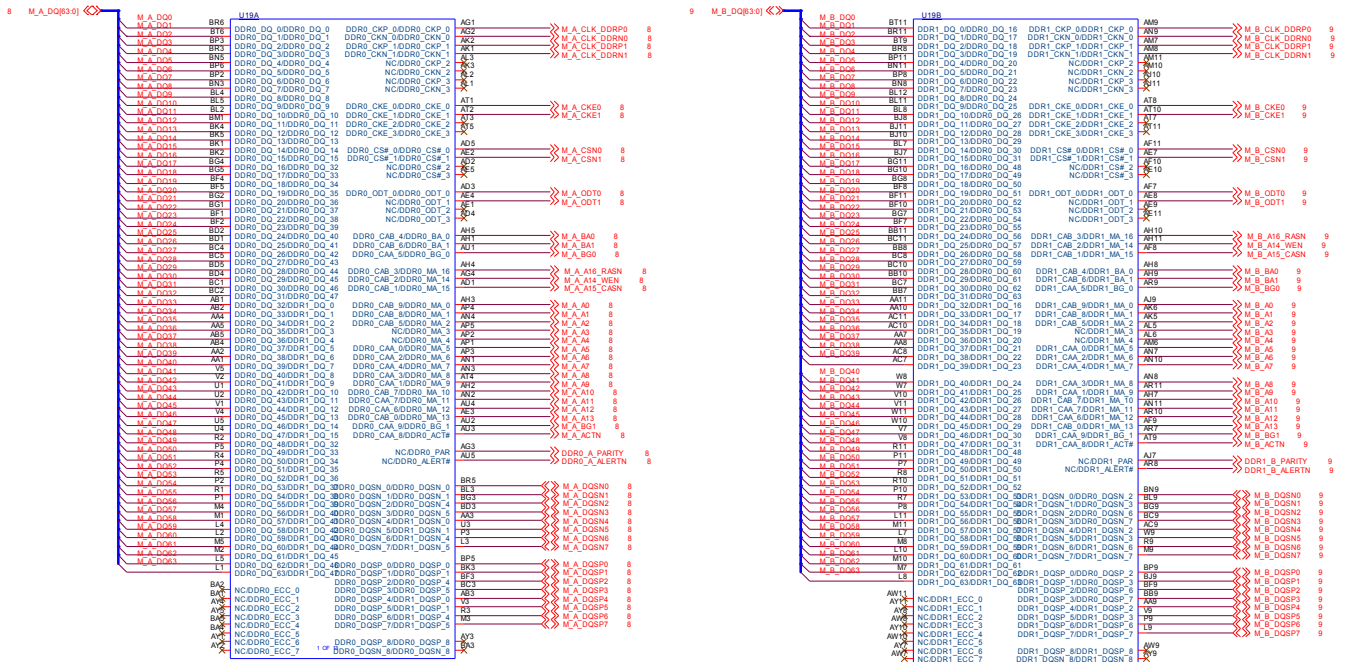


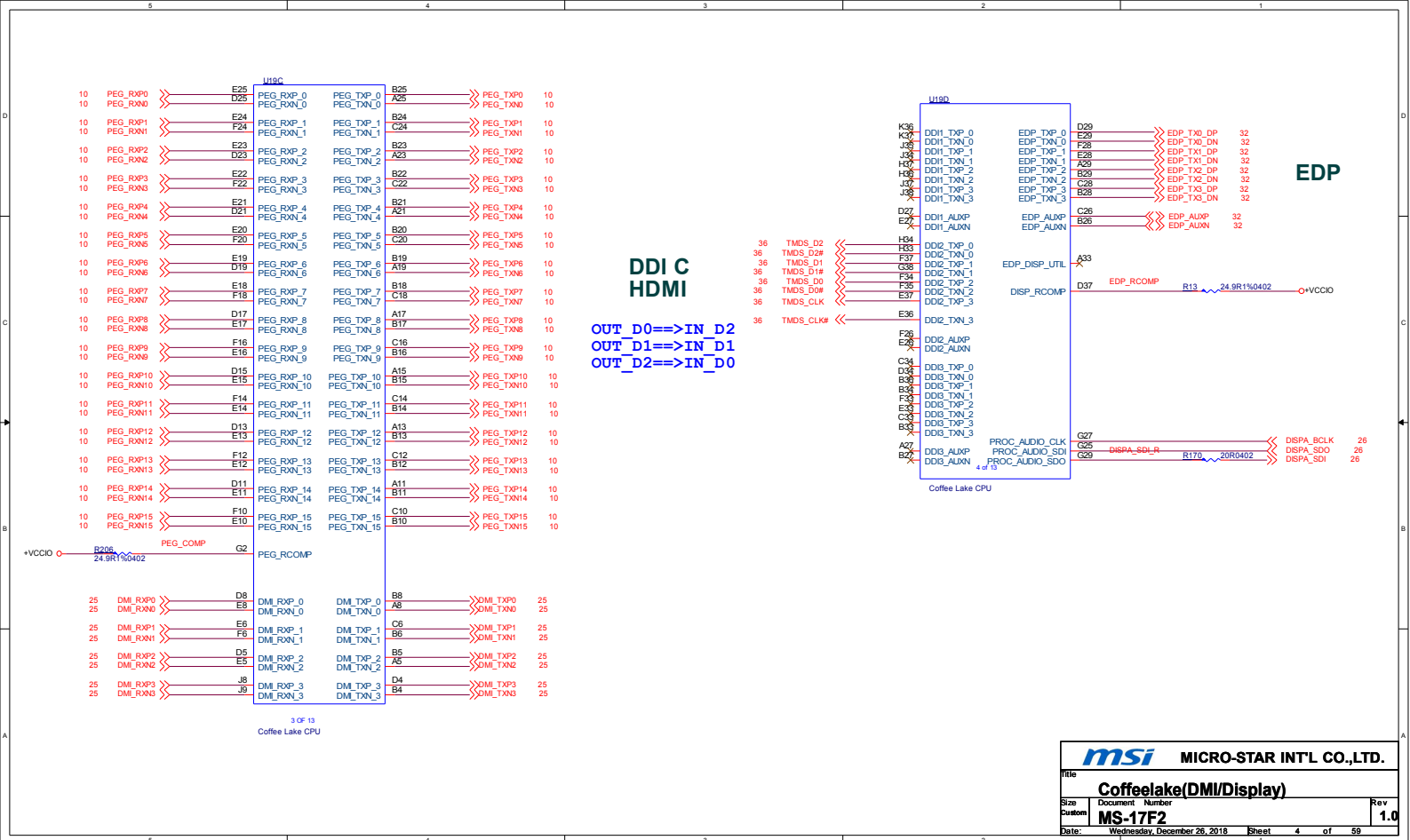
DDR Channel A

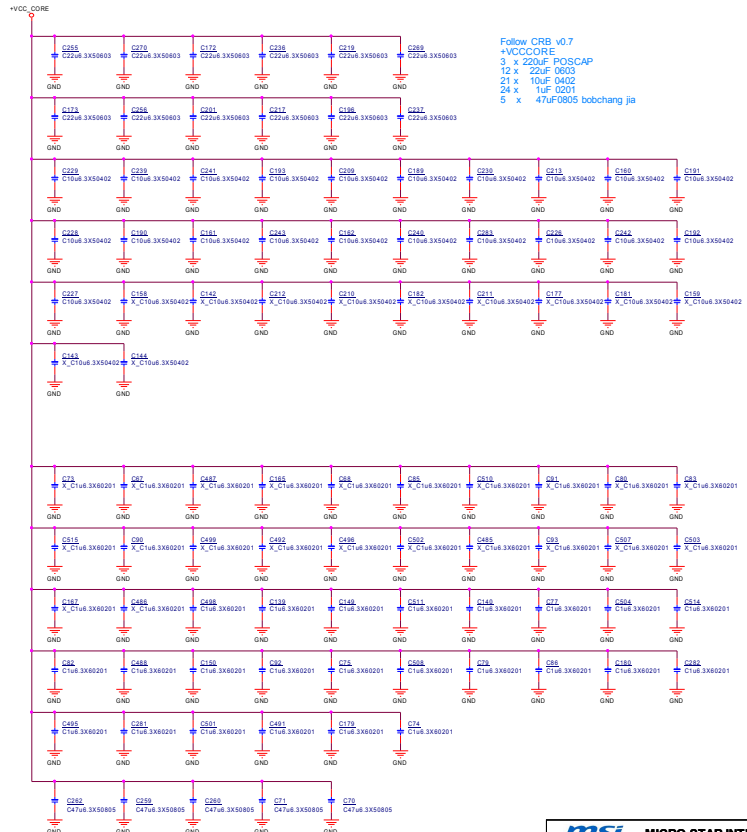
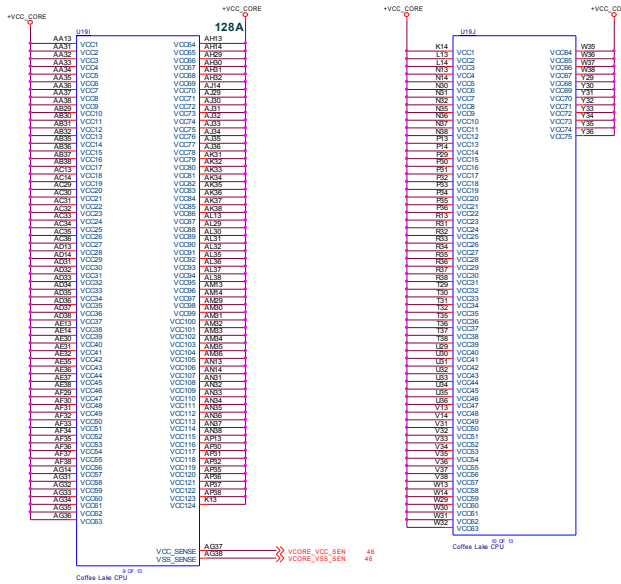
DDR Channel B



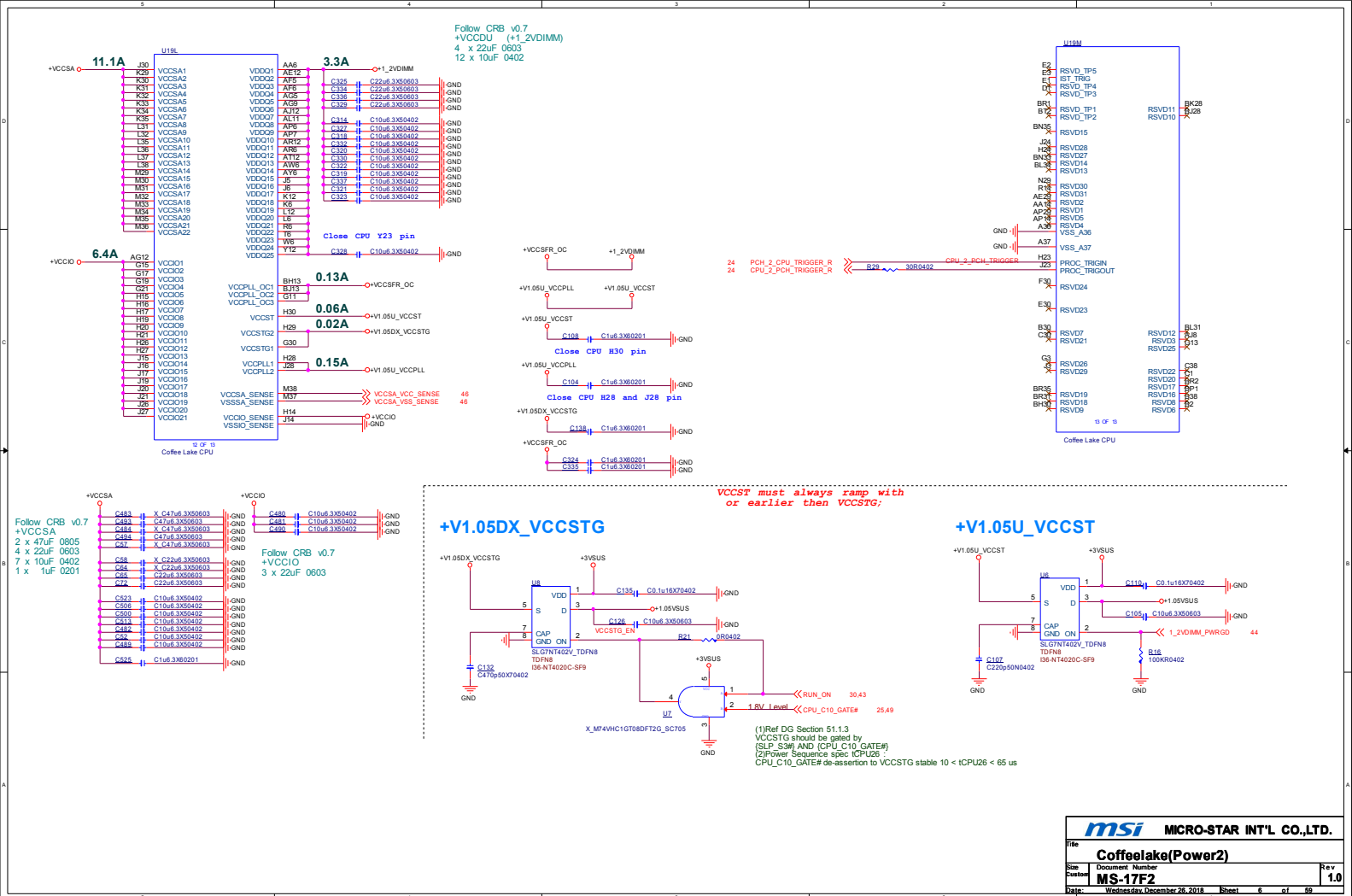
Coffee Lake CPU

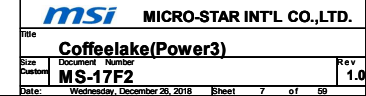
Coffee Lake CPU



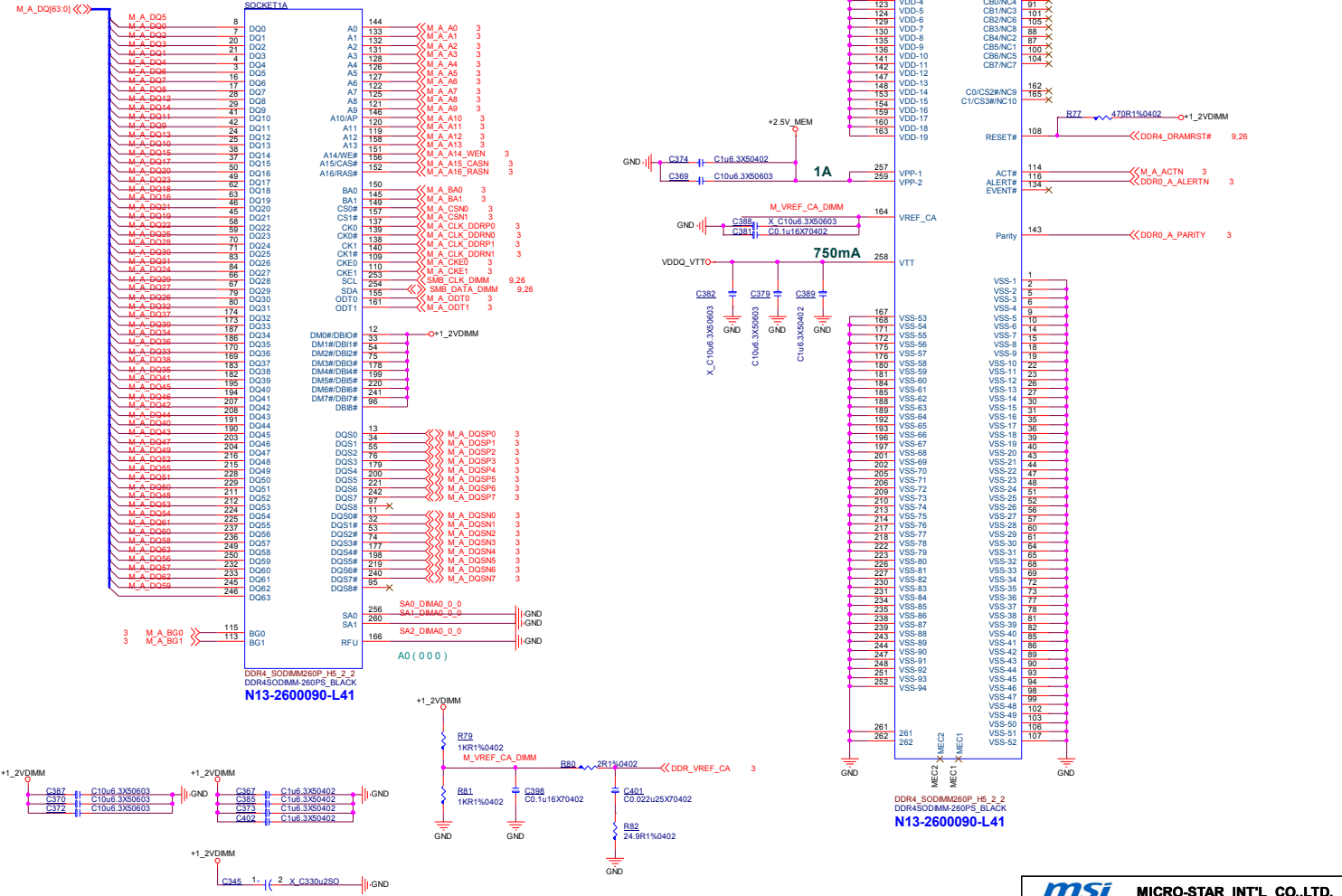


Follow CRB v0.7
+VCCORE
3 x 22uF POSCAP
12 x 22uF 0603
21 x 10uF 0402
24 x 1uF 0201
5 x 47uF0805 bobchang.jia

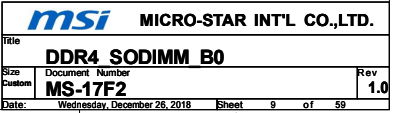




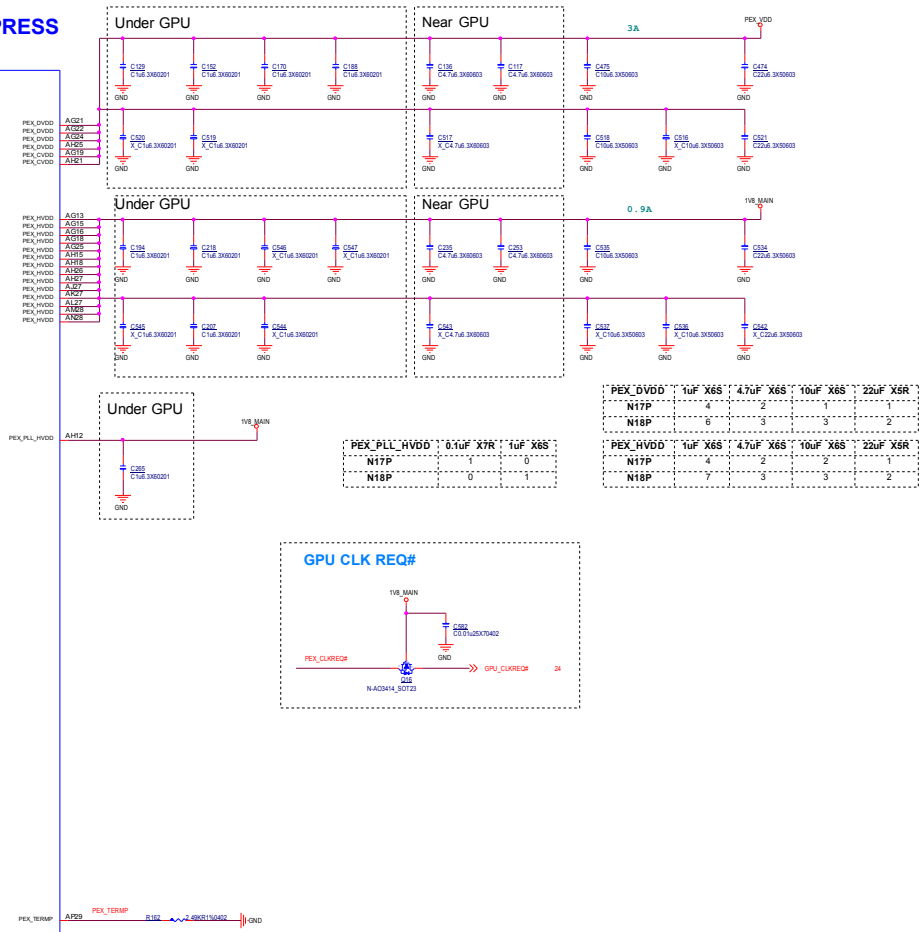
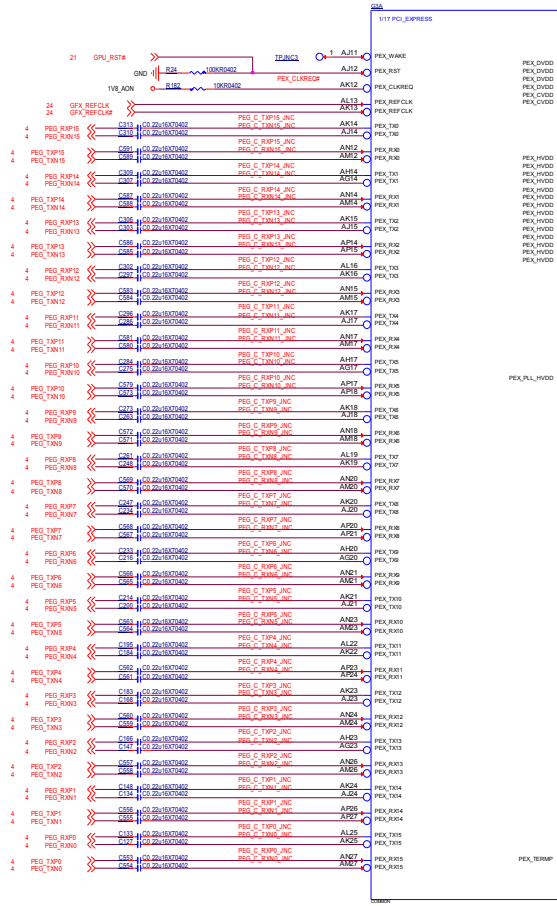
SODIMM_A0 (TOP-Reverse)



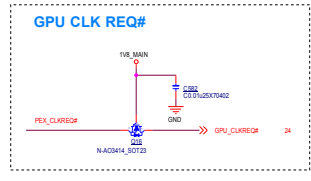
3 M_B_DQ[63:0] <<>



1/17 PCI_EXPRESS



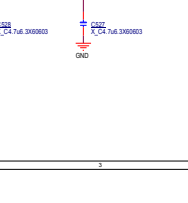
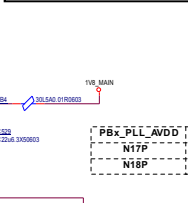
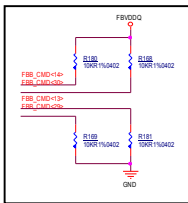
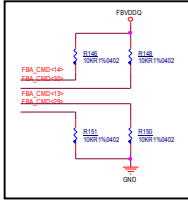
PEX_DVDD	1uF X6S	4.7uF X6S	10uF X6S	22uF X5R
N17P	4	2	1	1
N18P	6	3	3	
PEX_HVDD	1uF X6S	4.7uF X6S	10uF X6S	22uF X5R
N17P	4	2	2	1
N18P	7	3	3	2



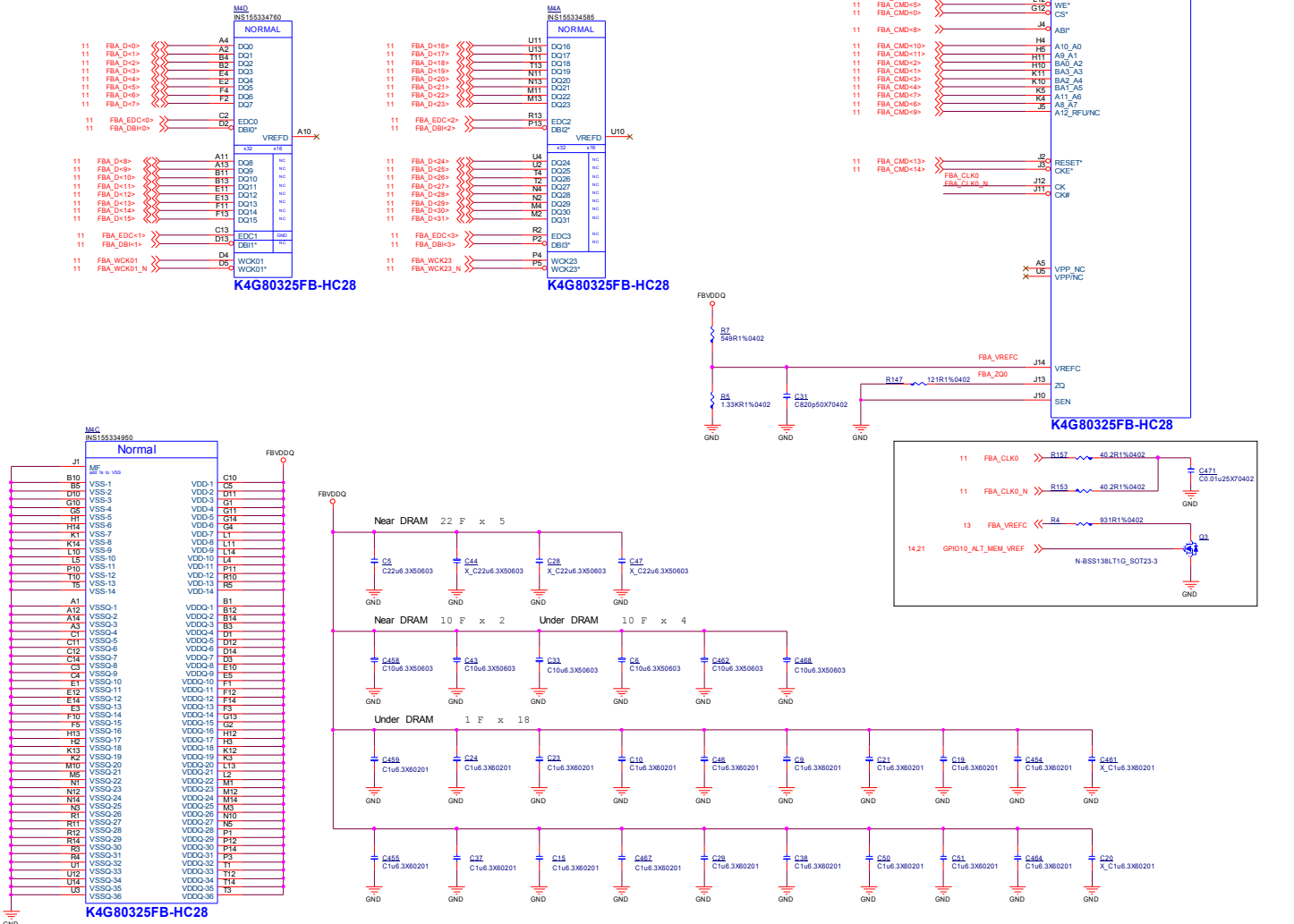
GPU Frame Buffer Partition A/B

GDD5 Command Mapping GB4C-128

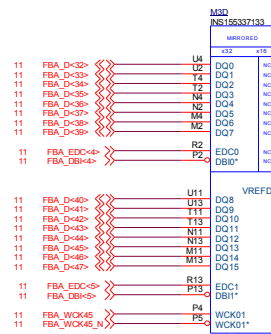
DQ[31:0]	DQ[63:32]	CS*
CMD0	CMD16	A3 BA3
CMD1	CMD17	A4 BA2
CMD2	CMD18	A5 BA1
CMD3	CMD19	WE*
CMD4	CMD20	A7 A8
CMD5	CMD21	A6 A11
CMD6	CMD22	ABT*
CMD7	CMD23	A12 RFU
CMD8	CMD24	A0 A10
CMD9	CMD25	A1 A9
CMD10	CMD26	RA5*
CMD11	CMD27	RST*
CMD12	CMD28	CKE*
CMD13	CMD29	CAS*
CMD14	CMD30	
CMD15	CMD31	



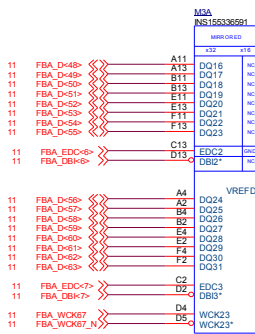
DGPU_GDDR5 FrameBuffer A0



DGPU_GDDR5 FrameBuffer A1

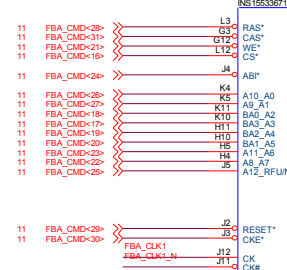


K4G80325FB-HC28

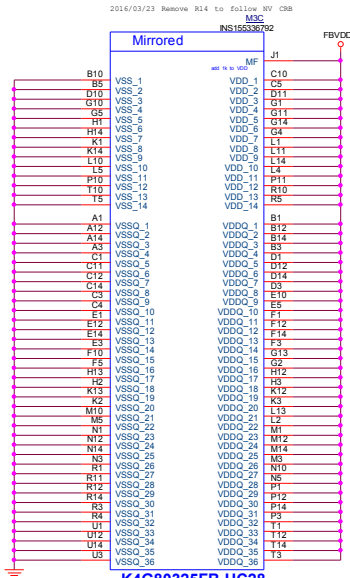


K4G80325FB-HC28

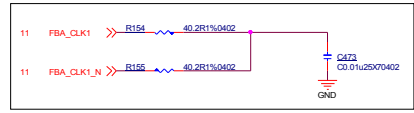
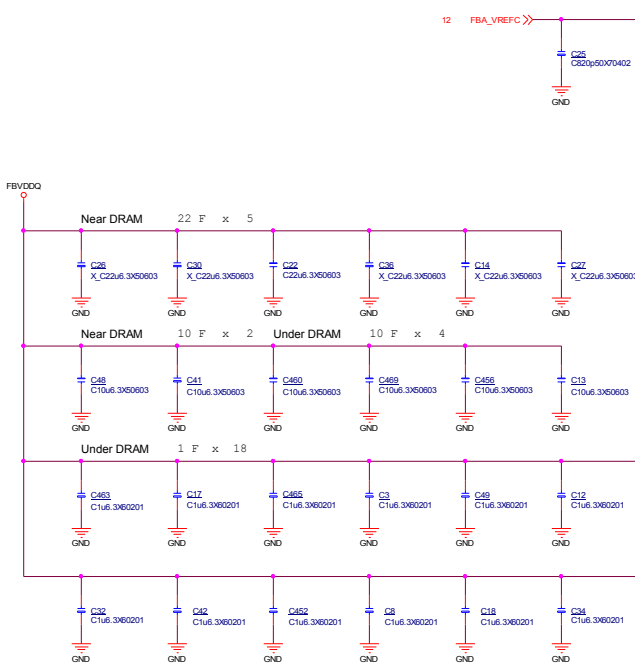
M12-8032545-S02



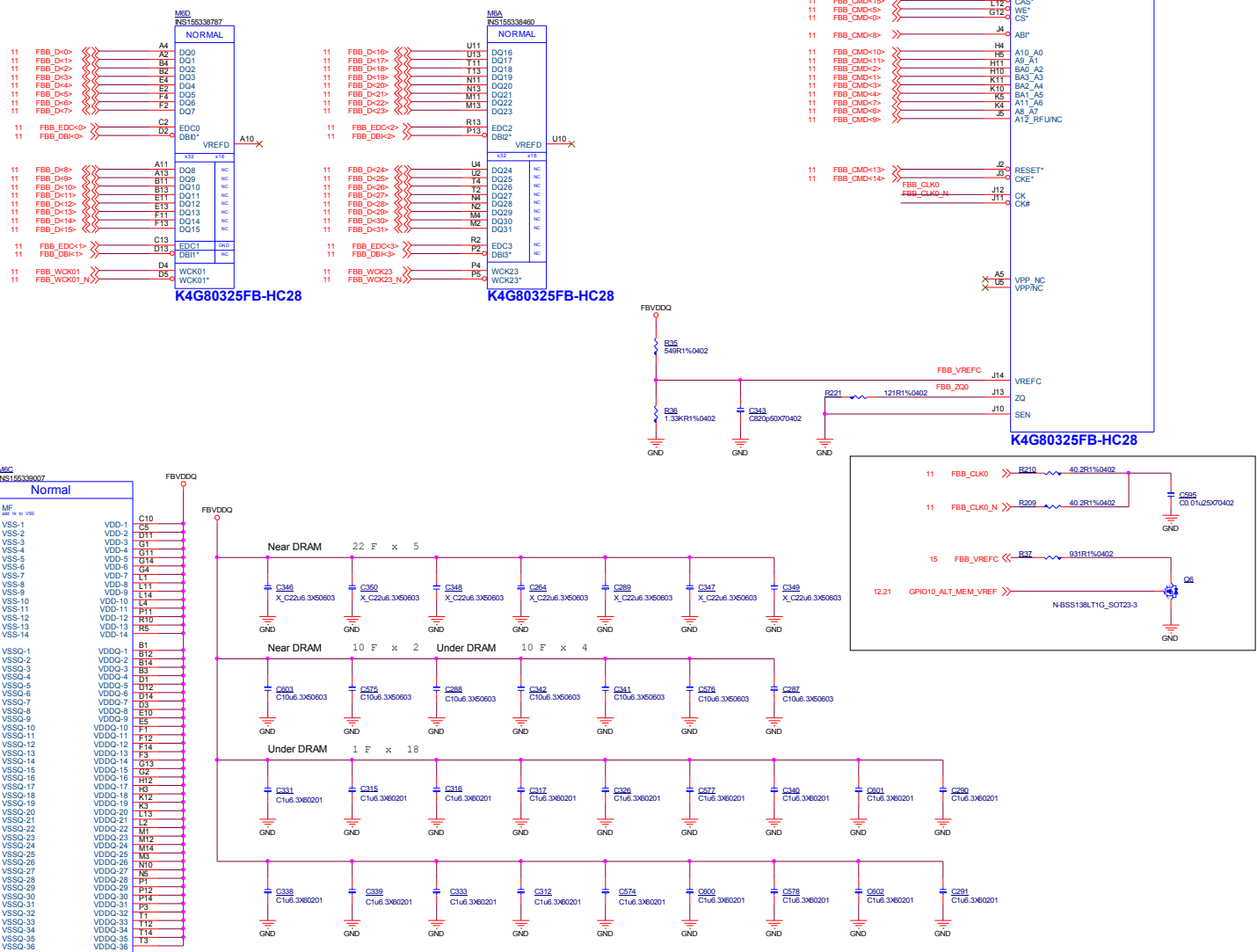
K4G80325FB-HC28




K4G80325FB-HC28

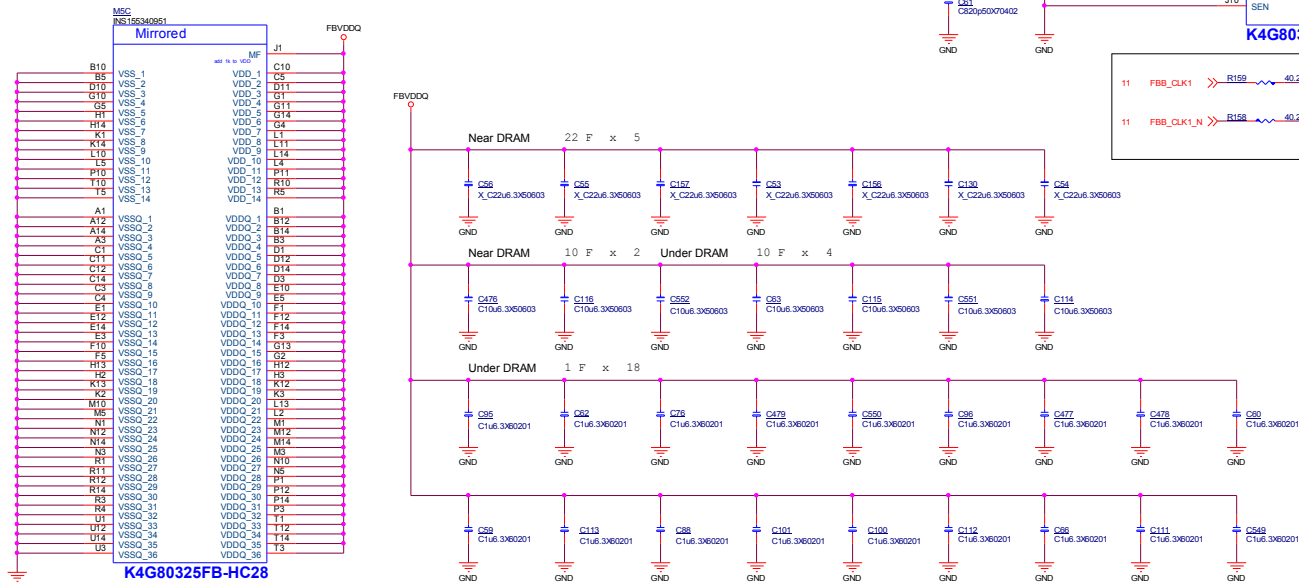
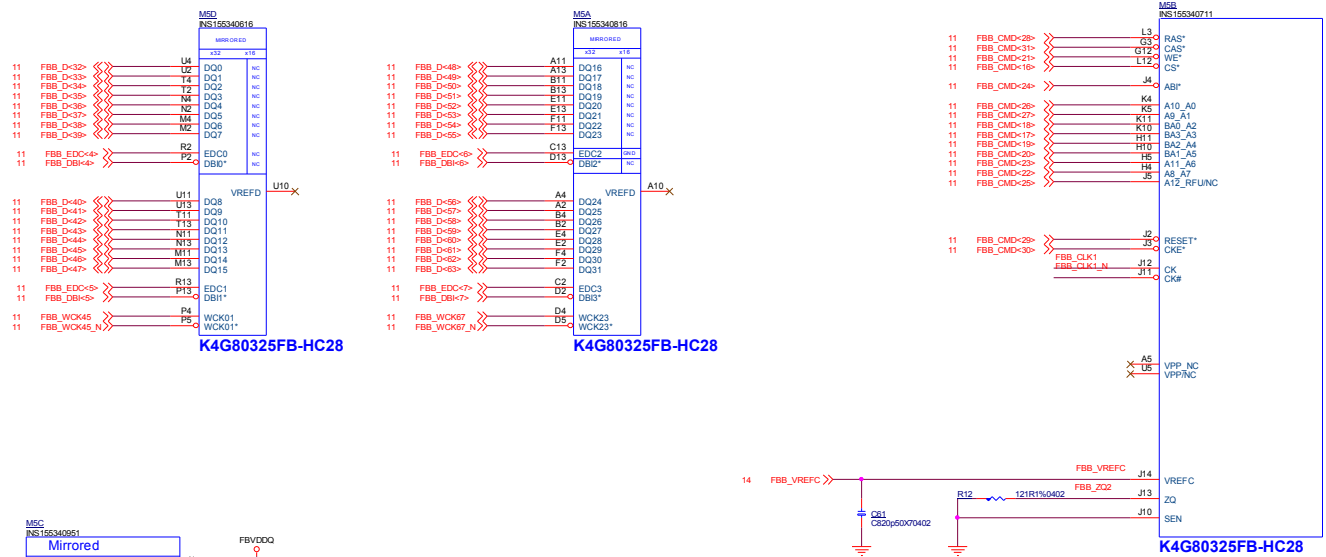


DGPU_GDDR5 FrameBuffer B0

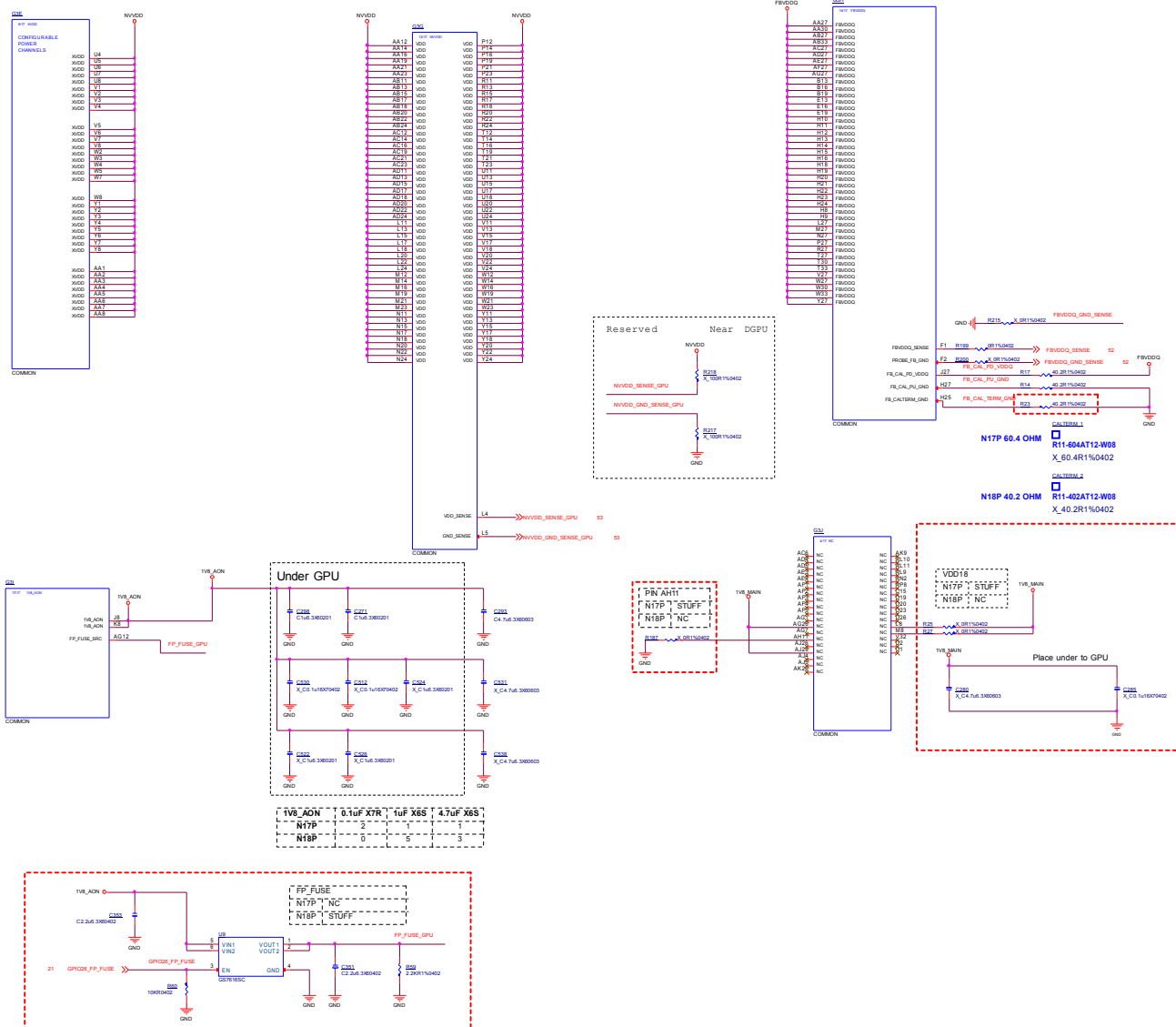


 MICRO-STAR INT'L CO.,LTD.	
title DGPU_GDDR5 FrameBuffer B0	
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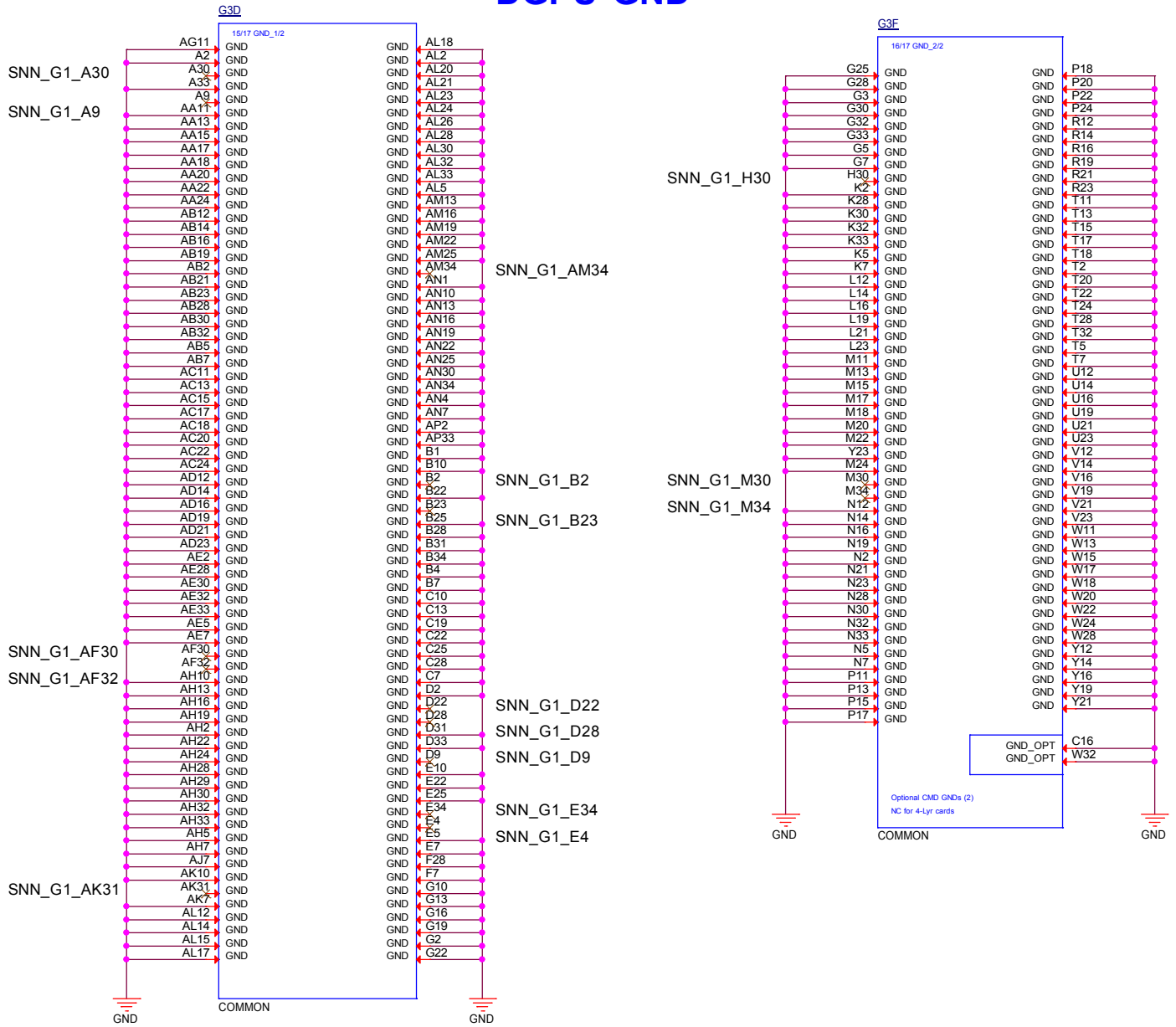
DGPU_GDDR5 FrameBuffer B1



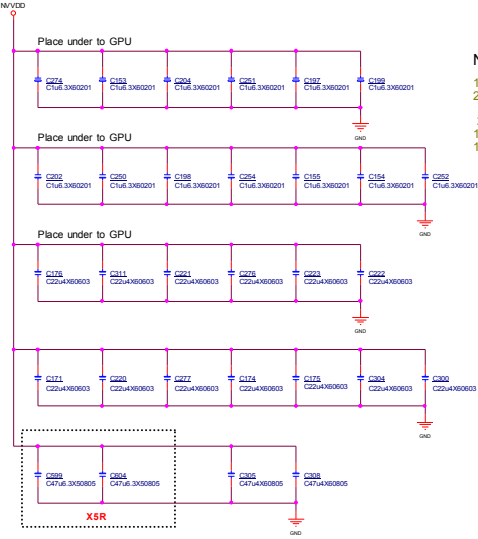
GPU NVVDD, FBVDDQ



DGPU GND

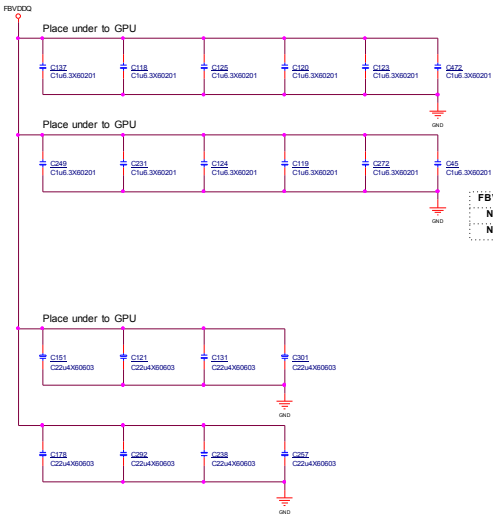


GPU DECOUPLING



NVVDD+NVDDS
13 x 1uF(Under GPU)
21 x 10uF(Under GPU)
2 x 4.7uF(Near GPU)
11 x 10uF(Near GPU)
10 x 22uF(Near GPU)

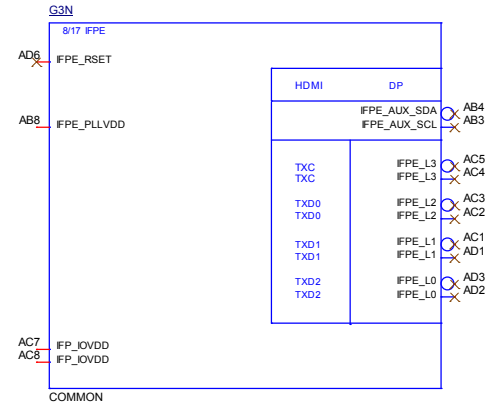
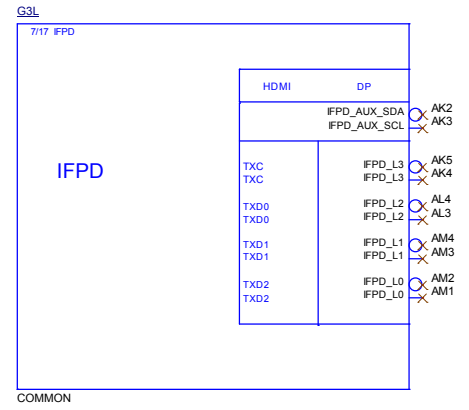
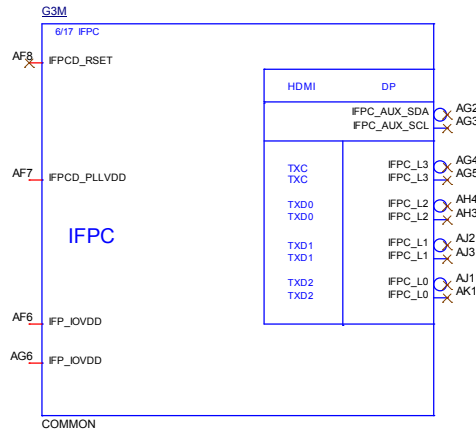
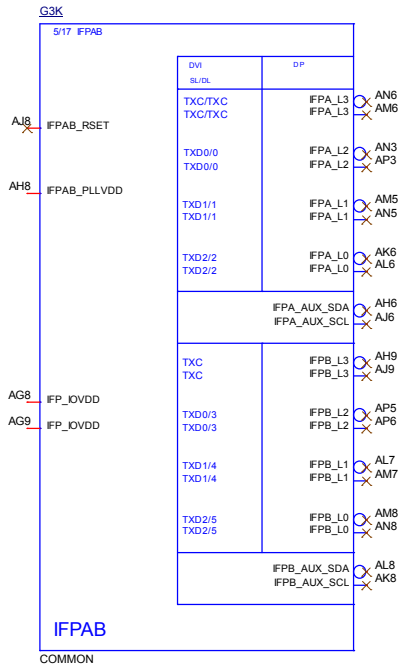
NVVDD	1uF X7R	4.7uF X6S	10uF X6S	22uF X6S
N17P	13	2	31	10
N18P	13	0	34	15



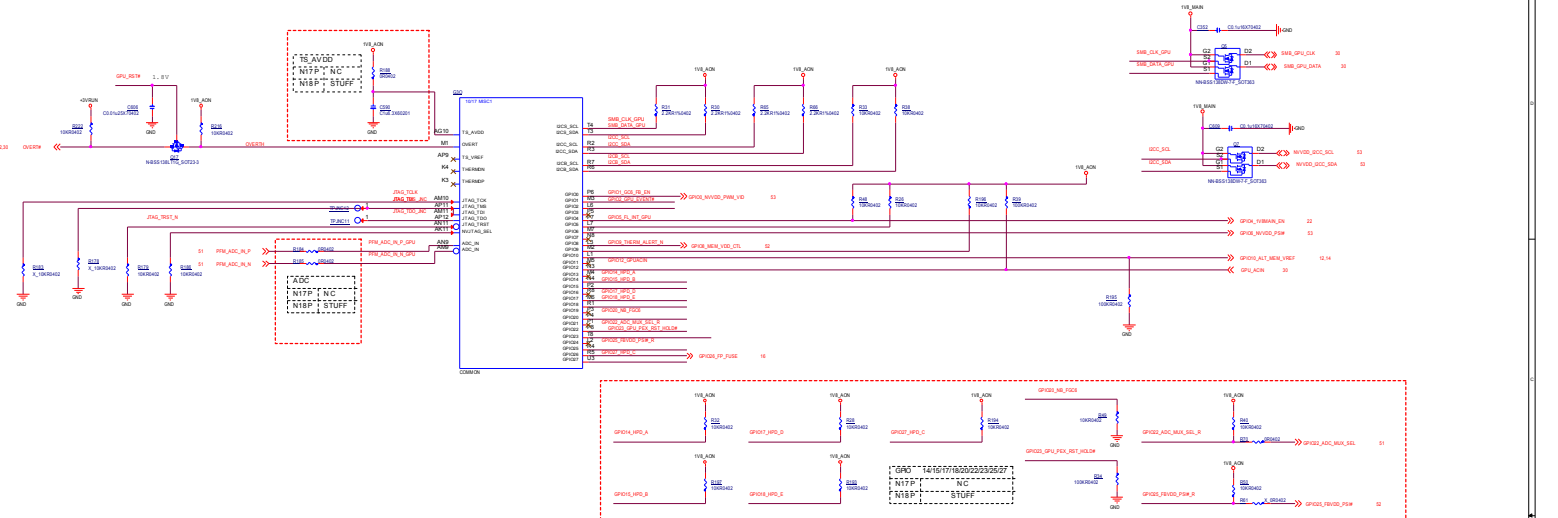
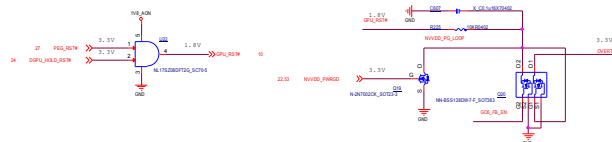
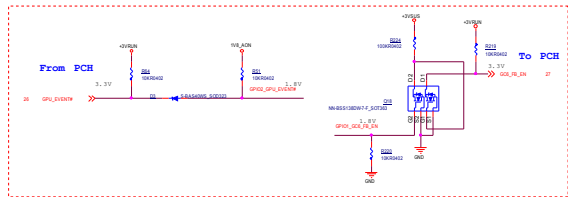
FBVDDQ
12 x 1uF(Under GPU)
4 x 10uF(Under GPU)
2 x 10uF(Near GPU)
5 x 22uF(Near GPU)

FBVDDQ	1uF X7R	10uF X6S	22uF X6S
N17P	12	6	5
N18P	12	6	5

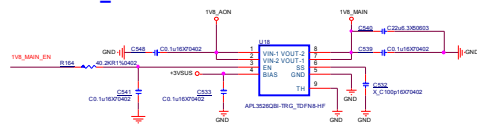
DACA,Display IF



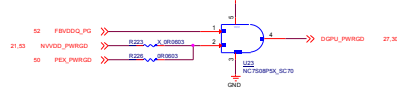
[illegible]

[illegible]

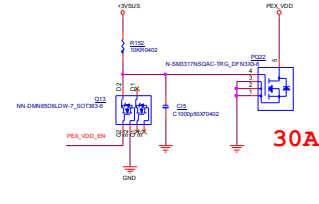
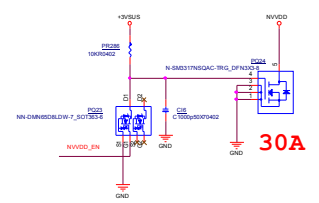
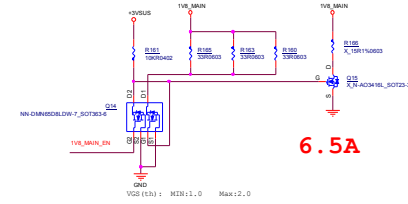
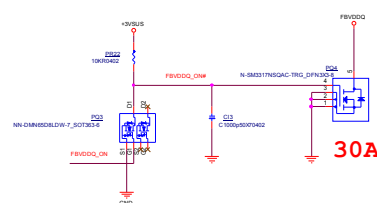
1V8_MAIN



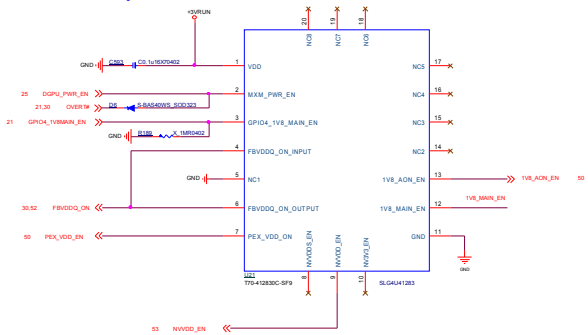
DGPU POWER GOOD



Discharge

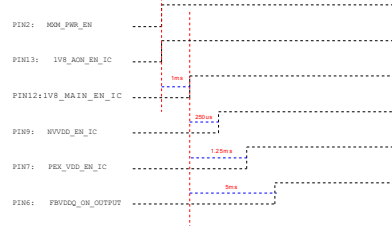


Power Sequence Control

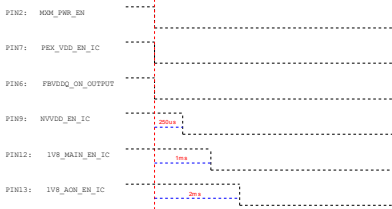


PIN2: MXM PWR EN is 3.3V
 PIN3: GPIO4 GC6 PWR EN is 1.8V
 PIN4: FBVDDQ_ON INPUT 3.3V
 PIN6: FBVDDQ_ON OUTPUT 3.3V
 PIN7: PEX_VDD_EN IC 3.3V
 PIN9: NVVDD_EN IC 3.3V
 PIN12: 1V8_MAIN_EN IC 3.3V
 PIN13: 1V8_AON_EN IC 3.3V

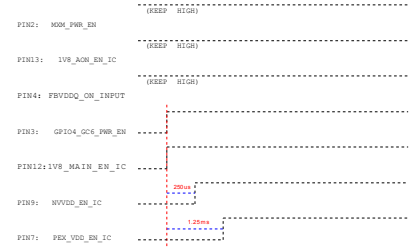
Power Up Sequence



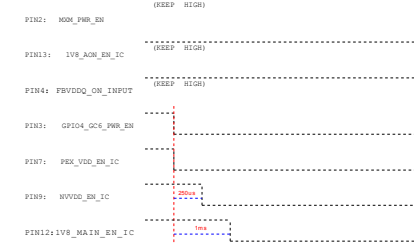
Power Down Sequence



GC6 2.1 Exit Sequence

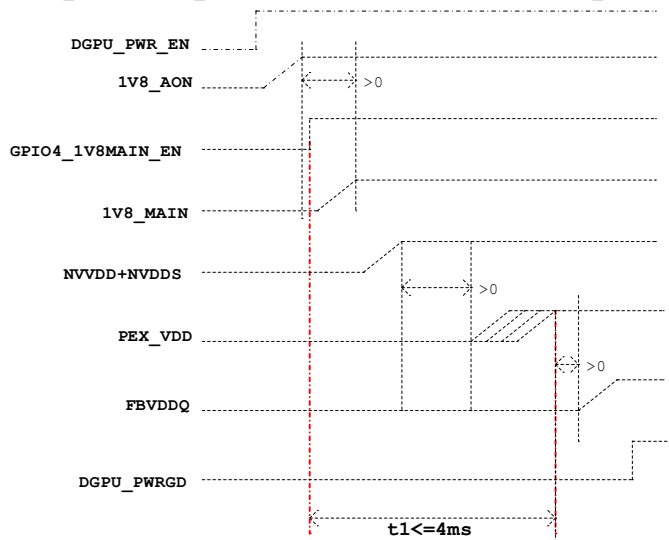


GC6 2.1 Entry Sequence

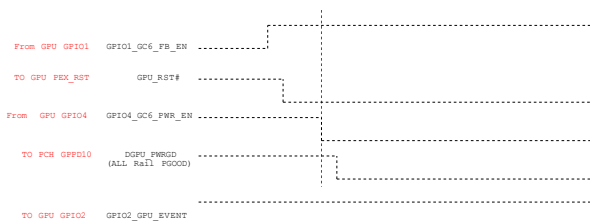


POWER UP Sequence

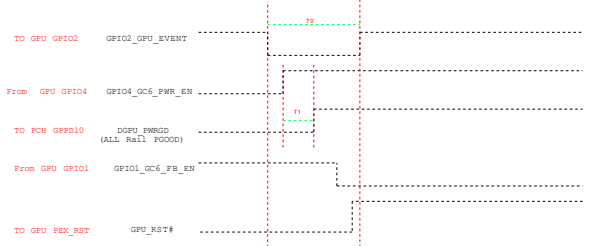
1V8_AON -> 1V8_MAIN->NV3V3 -> NVVDD -> NVVDDS / PEX_VDD -> FBVDDQ



GC6 2.1 ENTRY SEQUENCE



GC6 2.1 EXIT SEQUENCE



GC6 2.1 TIMING

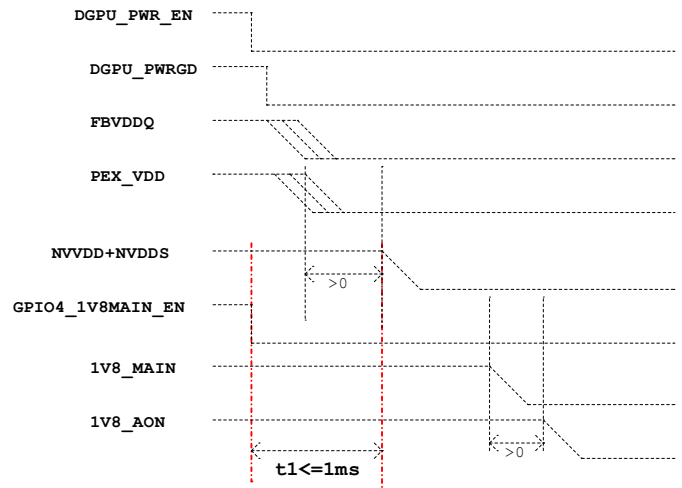
	Min	Max	Unit	Description
T0	0.001	N/A	mS	GPU_EVENT# assertion
T1	0.04	4	mS	3V3_MAIN_EN assertion to all power rails up and stable

NOTES:

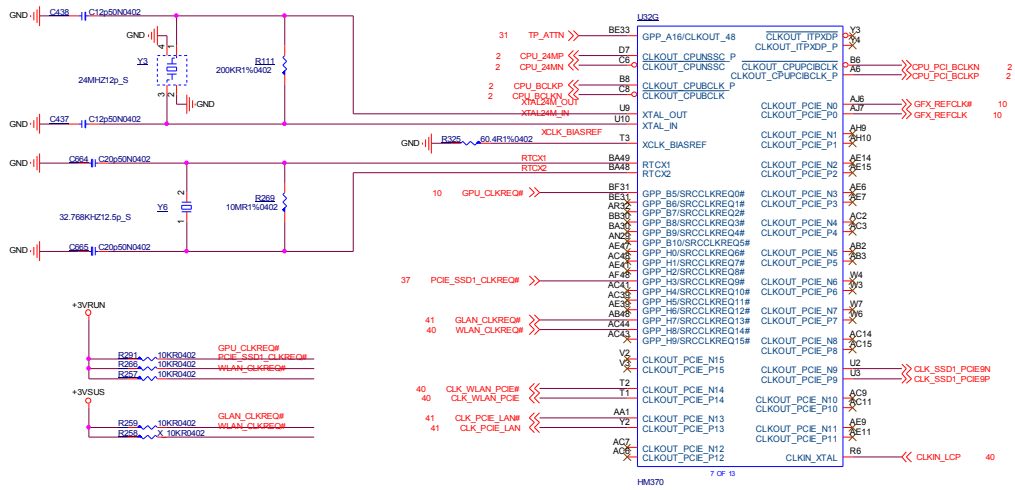
1. ALL RailPGOOD=1 represents all GPU power rails are ramped up and in regulation.
If any GPU power rail cannot be guaranteed in regulation this state should equal to 0.
2. During GC6 exit, the order of power rail ramp-up must follow the Power up sequence described in Chapter 3 with the exception that FBVDD/Q stays on.
3. All delays should be minimized to increase time spent in GC6 for maximum power saving.
4. The entire entry and exit sequence must complete within 200 ms.

POWER Down Sequence

NVVDDS/PEX_VDD/FBVDDQ -> NVVDD/NV3V3->1V8_MAIN-> 1V8_AON



HM370 (RTC/PCIE_Clock/Clock/RSVD)



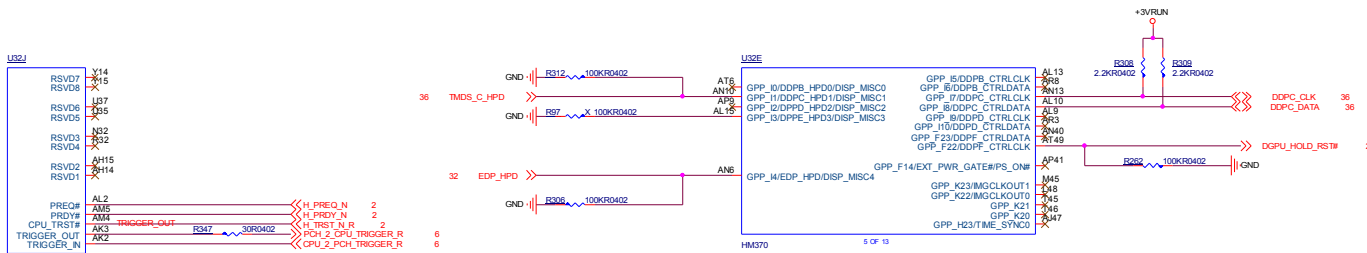
Functional Strap Definitions


DDPB_CTRLDATA / GPP_I6
This signal has a weak internal pull-down.
0 = Port B is not detected. (Default)
1 = Port B is detected.

DDPC_CTRLDATA / GPP_I8
This signal has a weak internal pull-down.
0 = Port B is not detected. (Default)
1 = Port B is detected.

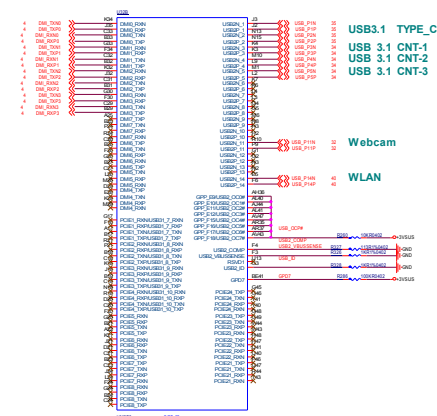
DDPD_CTRLDATA / GPP_I10
This signal has a weak internal pull-down.
0 = Port B is not detected. (Default)
1 = Port B is detected.

GPP_F23
This signal has a weak internal pull-down.
0 = Port F is not detected. (Default)
1 = Port F is detected.



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HM370 (DMI/PCIE/USB3.1/USB2.0/CNV)



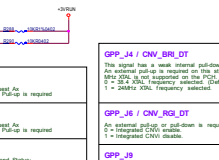
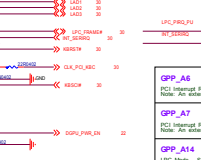
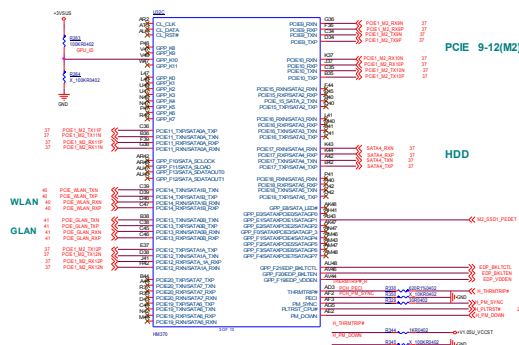
USB 2.0	USB 3.1	Device	Note
1	1	USB TYPE-C-1	
2	3	USB TYPE-C-2	
3	3	USB TYPE-A-1	
4	4	USB TYPE-A-2	
5	5	USB TYPE-A-3	
6			
7			
8			
9			
10			
11			
12			
13			
14			

High Speed I/O Ports		Device
1	2	
1	USB3.1 Gen 1	NC
2	USB3.1 Gen 1	NC
3	NA	NC
4	NA	NC
5	INTEL LAN Only	NC
6	NA	NC
7	NA	NC
8	NA	NC
9	PCIeLAN	PCIe Configurable M2
10	PCIe	M2 SSD-1
11	PCIeSATA1A	
12	PCIeLAN/SATA1B	
13	PCIeLAN/SATA1B	LAN
14	PCIeSATA1B	WLAN
15	PCIe	NC
16	PCIe	NC
17	PCIeSATA4	HDD
18	PCIeSATA5	NC
19	PCIe	NC
20	PCIe	NC
21	PCIe	NC
22	PCIe	NC
23	PCIe	NC
24	PCIe	NC

GPD7
External pull-up is required. Recommended 100k.
This strap should remain float. There should NOT be any isolated device driving it to opposite direction during strap sampling.

SATA Lane 0 has the flexibility to be mapped to PCIe 11 or 13
SATA Lane 1 has the flexibility to be mapped to PCIe 12 or 14

GPP_K10	W1TP	PULL LOW
W1TP		
W1TP		

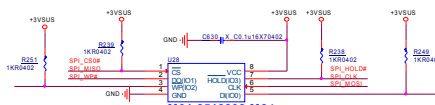


HM370 (UART/I2C/SPI)



SPI FLASH ROM

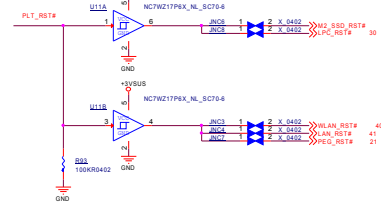
16MB



M31-2512832-M24

Supported types of Flash Memory
Command:0x03 & 0x0B & 0xBB

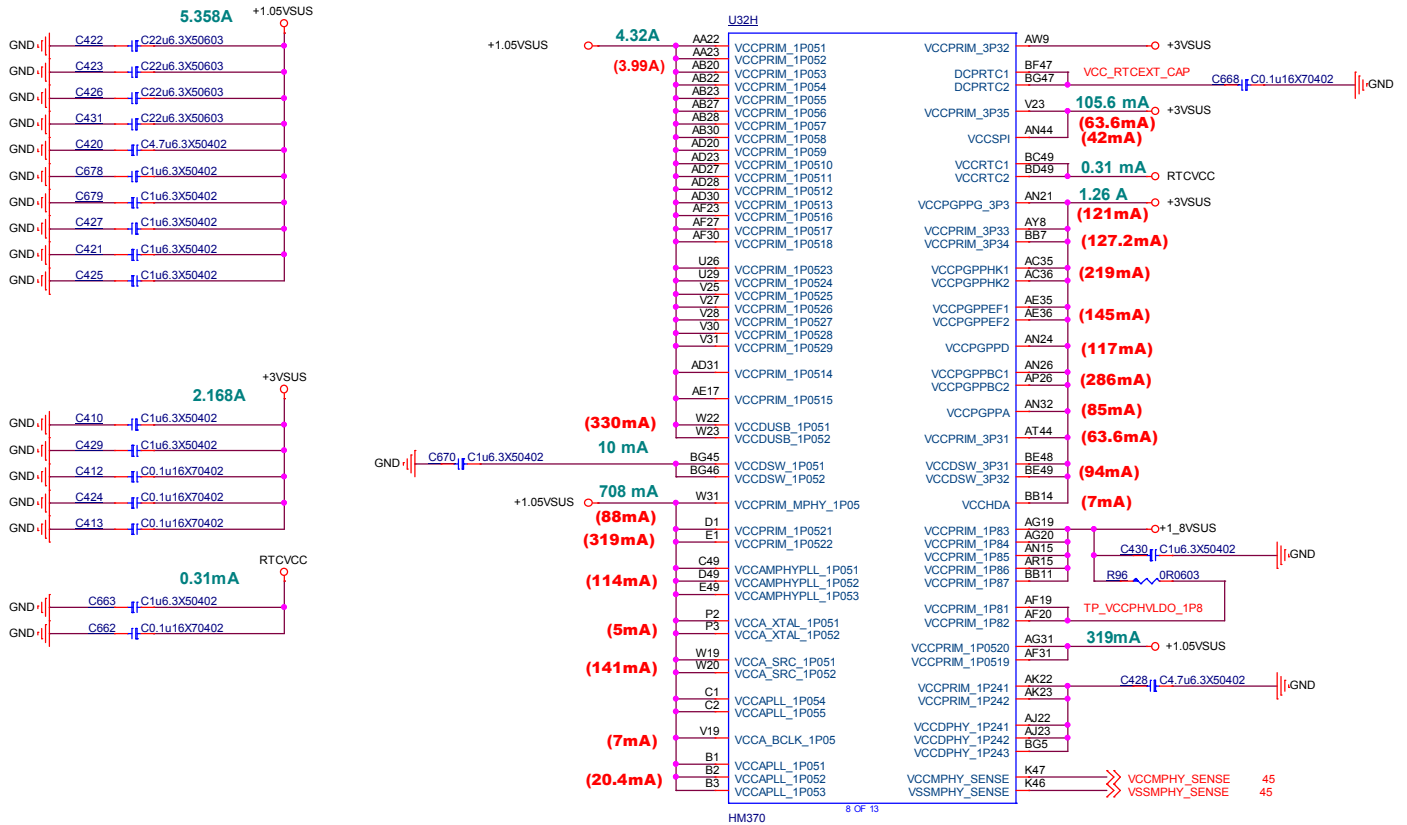
PLT_RST#



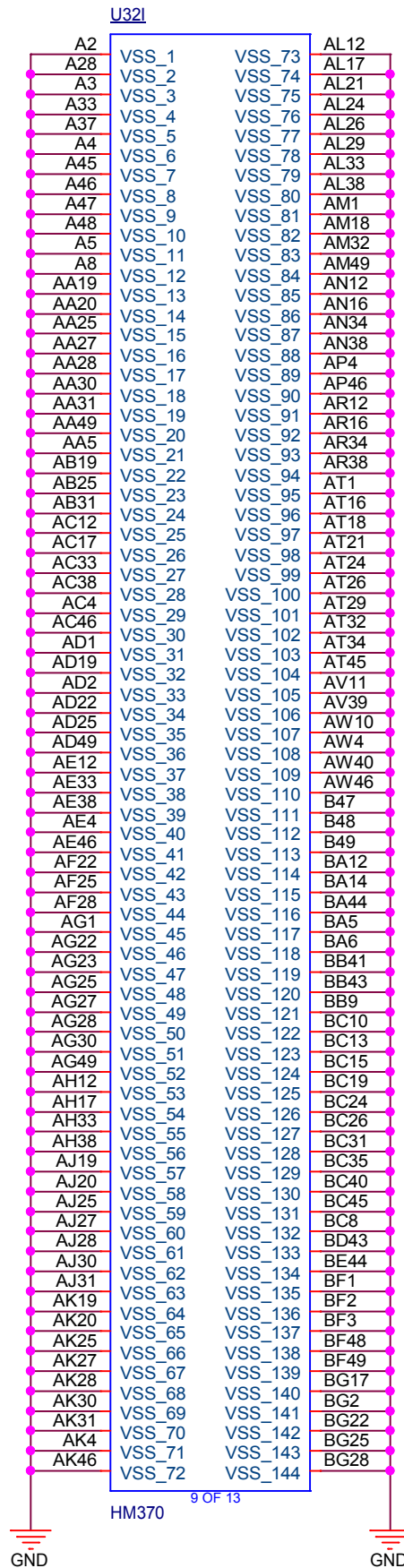
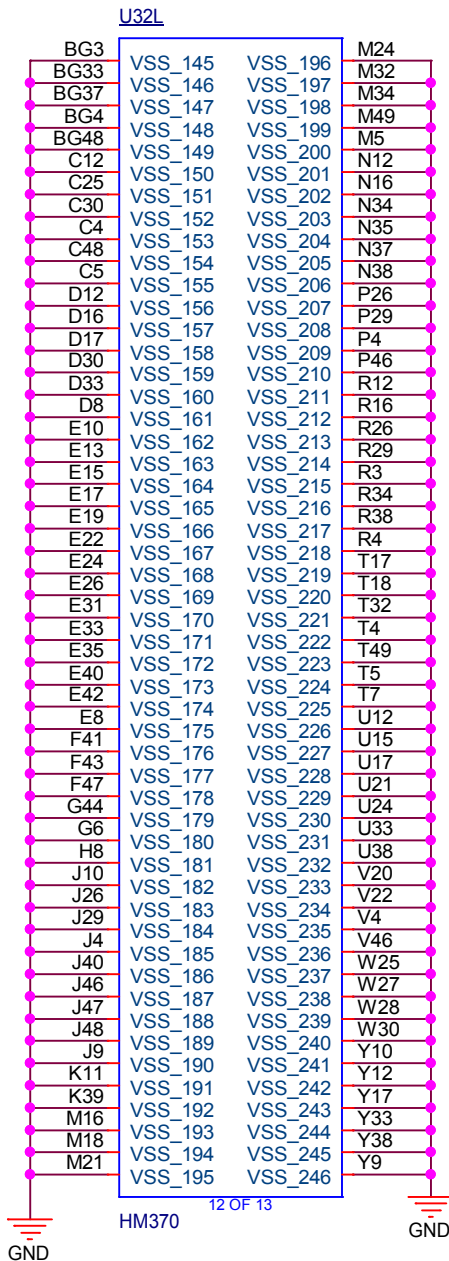
Functional Strap Definitions

SML3ALERT# / GPP_H15
External pull-up is required. Recommend 100K if pulled up to 3.3V or 75K if pulled up to 1.8V.
GSPH1_MOSI / GPP_B22
This Signal has a weak internal pull-down. Bit 0 Boot BIOS Destination SPI (Default) 1 LPU
GSPH0_MOSI / GPP_B18
This signal has a weak internal pull-down. 0 = Disable No Reboot mode. (Default) 1 = Enable No Reboot mode

HM370 (Power)



PCH-H(GND)



MICRO-STAR INT'L CO.,LTD.

Title

PCH-6(GND)

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Custom

MS-17F2

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1.0

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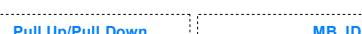
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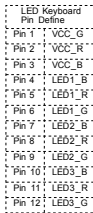
U12		
-----	--	--


A B



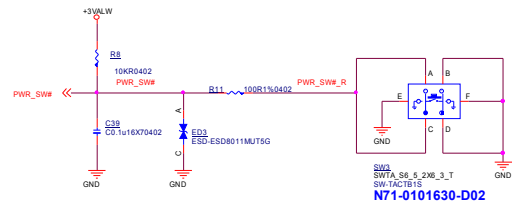
SMB_GPU_DATA R137 3.3K
SMB_GPU_CLK R138 3.3K

PQ213
X 100K90402

☐ LED Keyboard

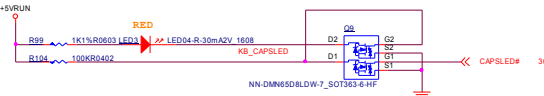
		MICRO-STAR INT'L CO.,LTD.	
EC(ENE9028)			
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Power Switch

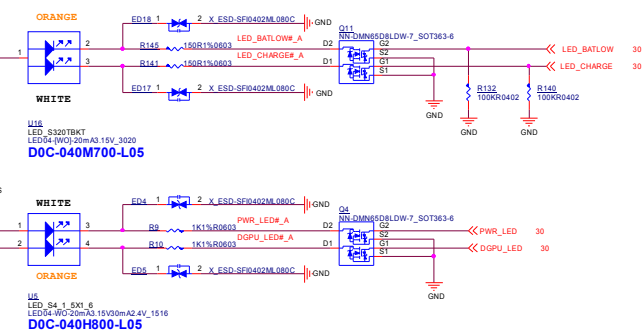


17F1 LED2

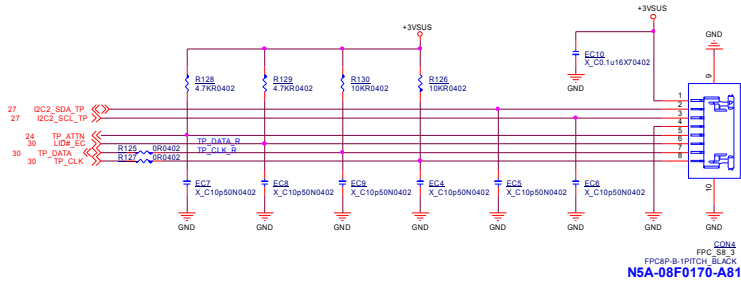
CAPSLED



LED

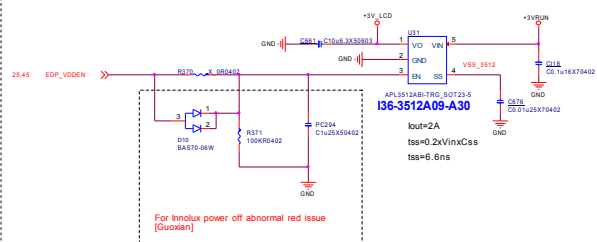


Touch Pad Board

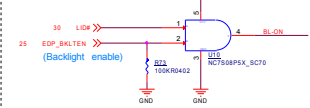


msi MICRO-STAR INT'L CO.,LTD.	
File	Power Switch/FAN/Touch Pad/LED
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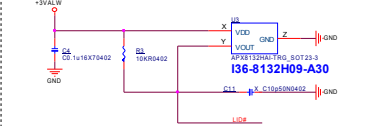
Pannel Device Logic Power



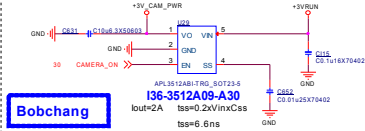
Backlight



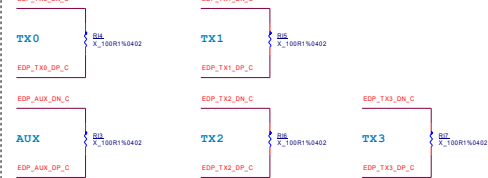
Hall Switch



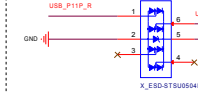
CAMERA Power



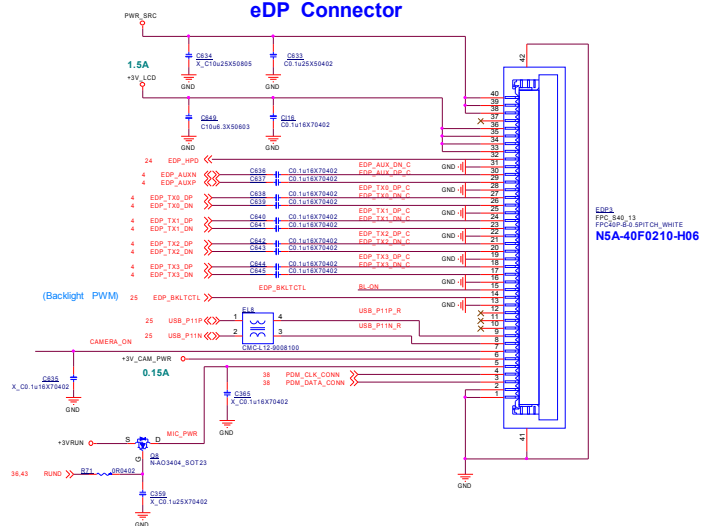
EMI Close Connector



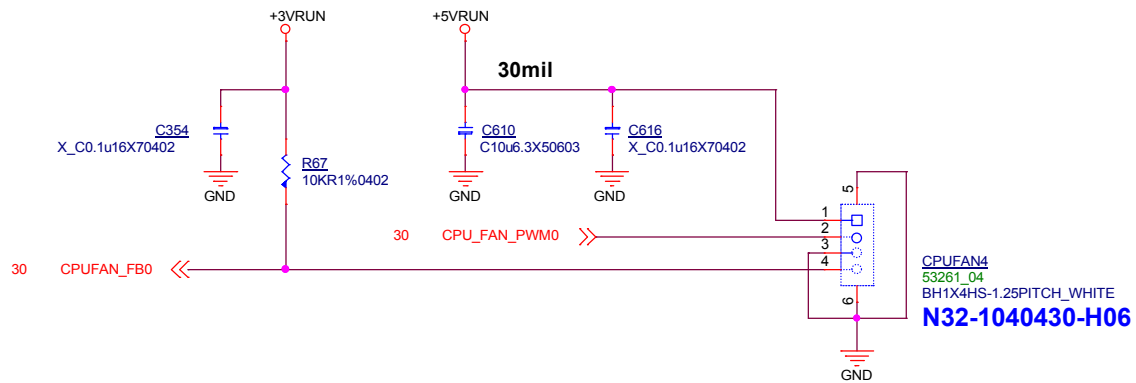
ESD



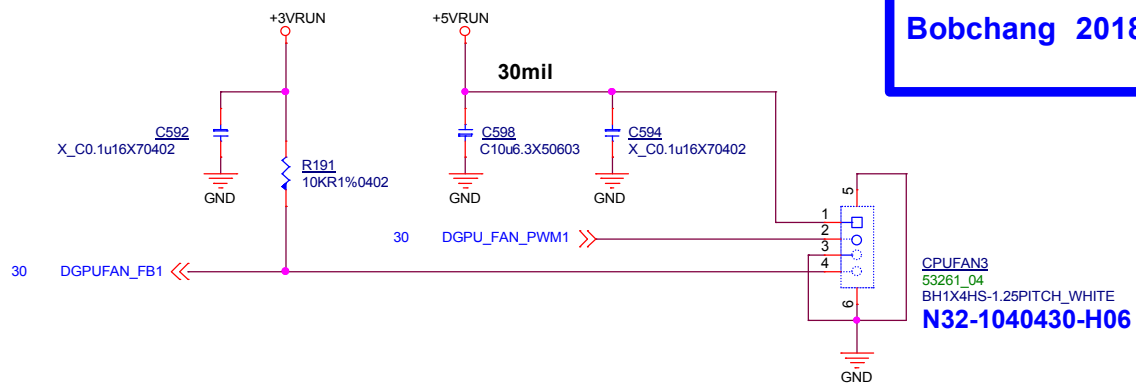
eDP Connector



CPU FAN



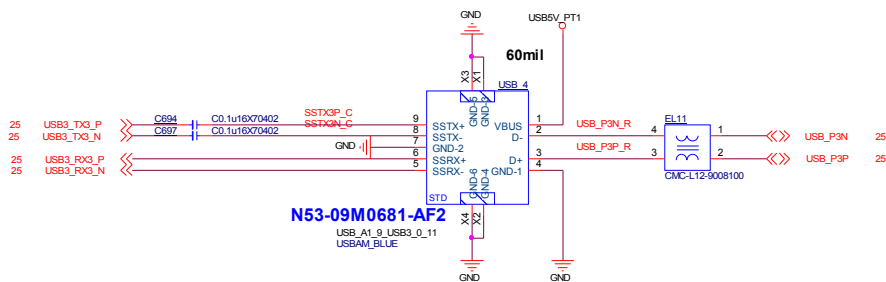
DGPU FAN



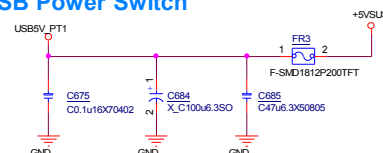
Bobchang 20180612

msi MICRO-STAR INT'L CO.,LTD.	
Title FAN	
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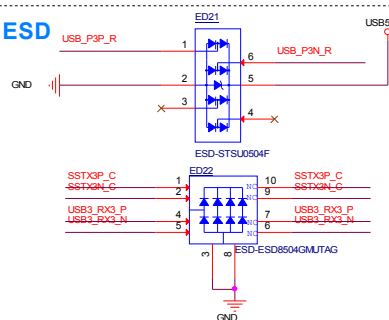
USB3.0 CNT-1



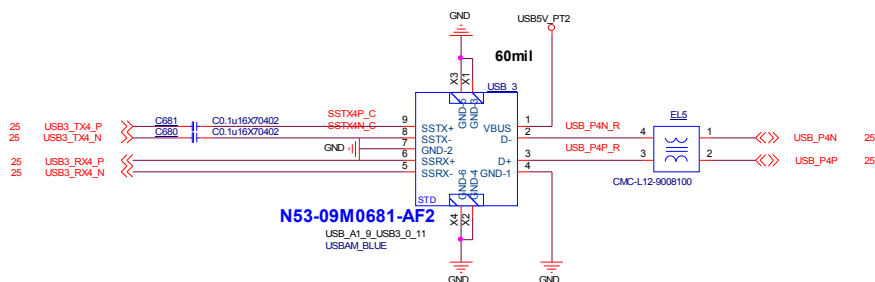
USB Power Switch



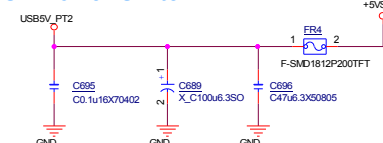
ESD



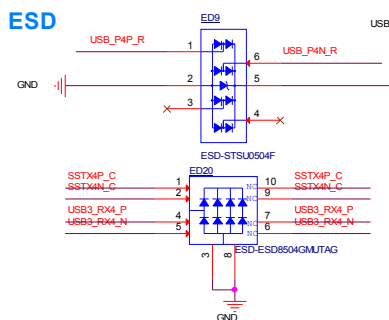
USB3.0 CNT-2



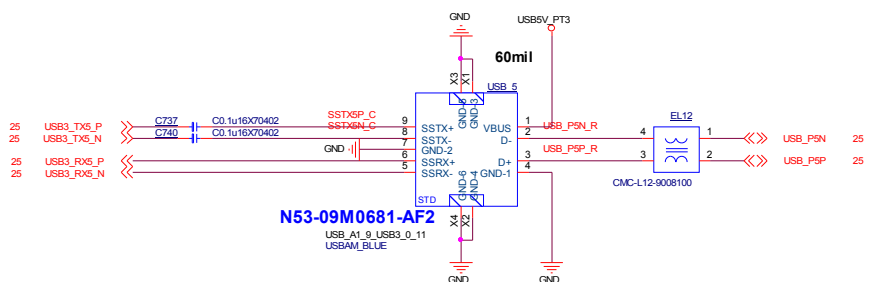
USB Power Switch



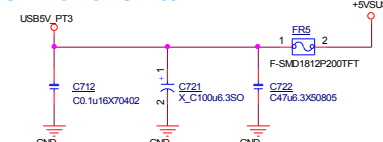
ESD



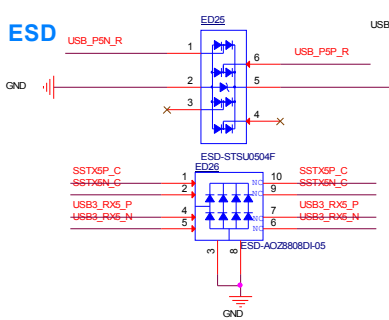
USB3.0 CNT-3



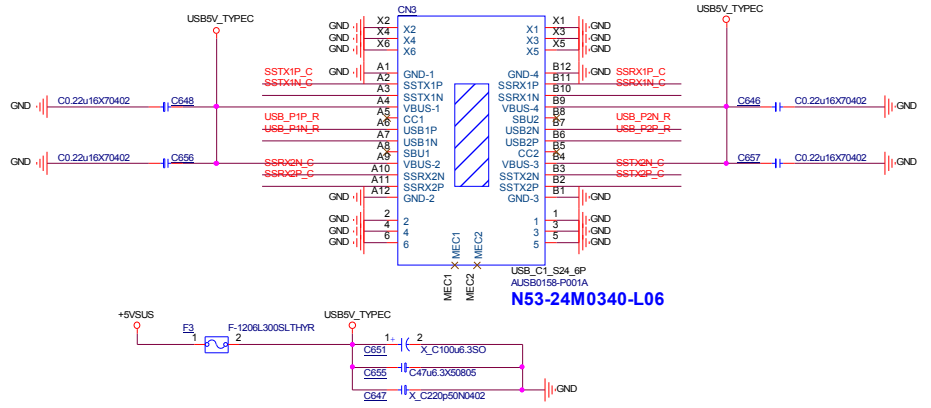
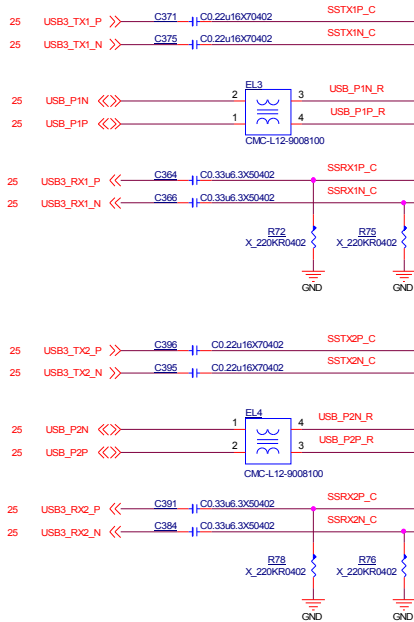
USB Power Switch



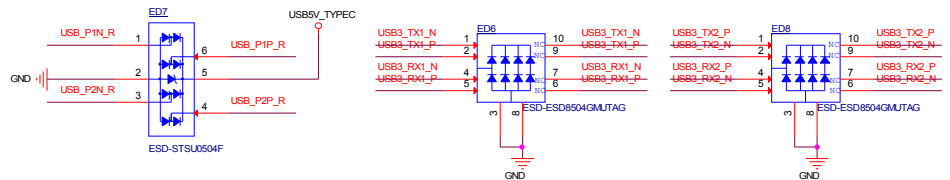
ESD



USB 3.0 TYPE_C

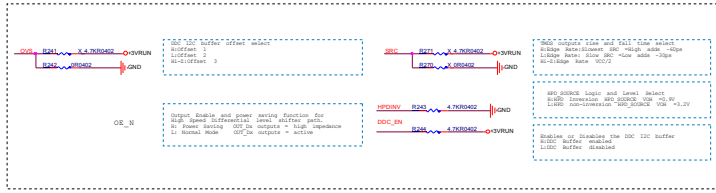


ESD

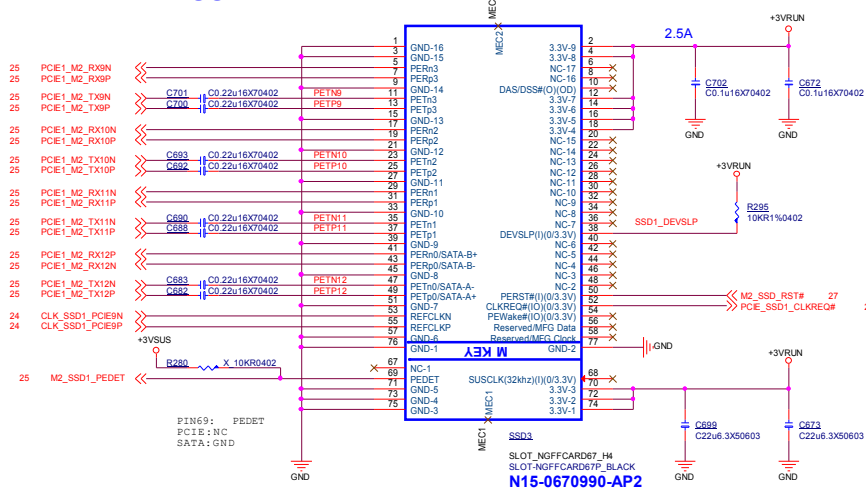


msi MICRO-STAR INT'L CO.,LTD.			
Title			
USB 3.0 TYPE_C			
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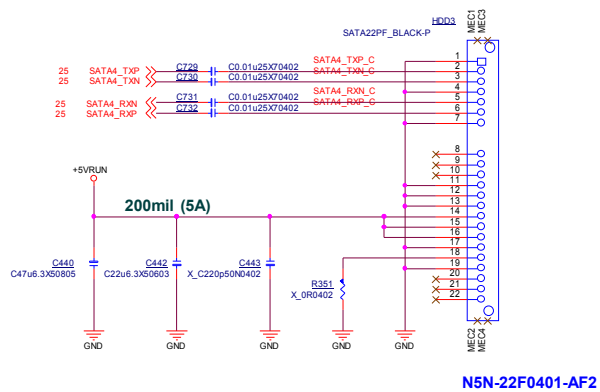
Bobchang 20180613

[illegible]


M.2 SSD **PCIEx4 /SATA**



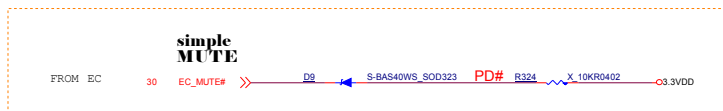
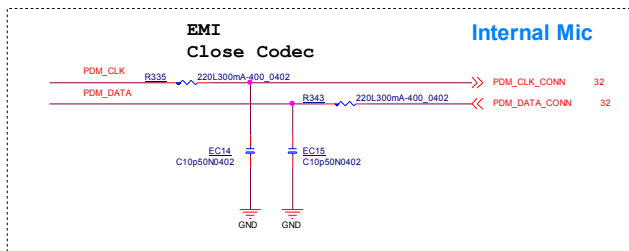
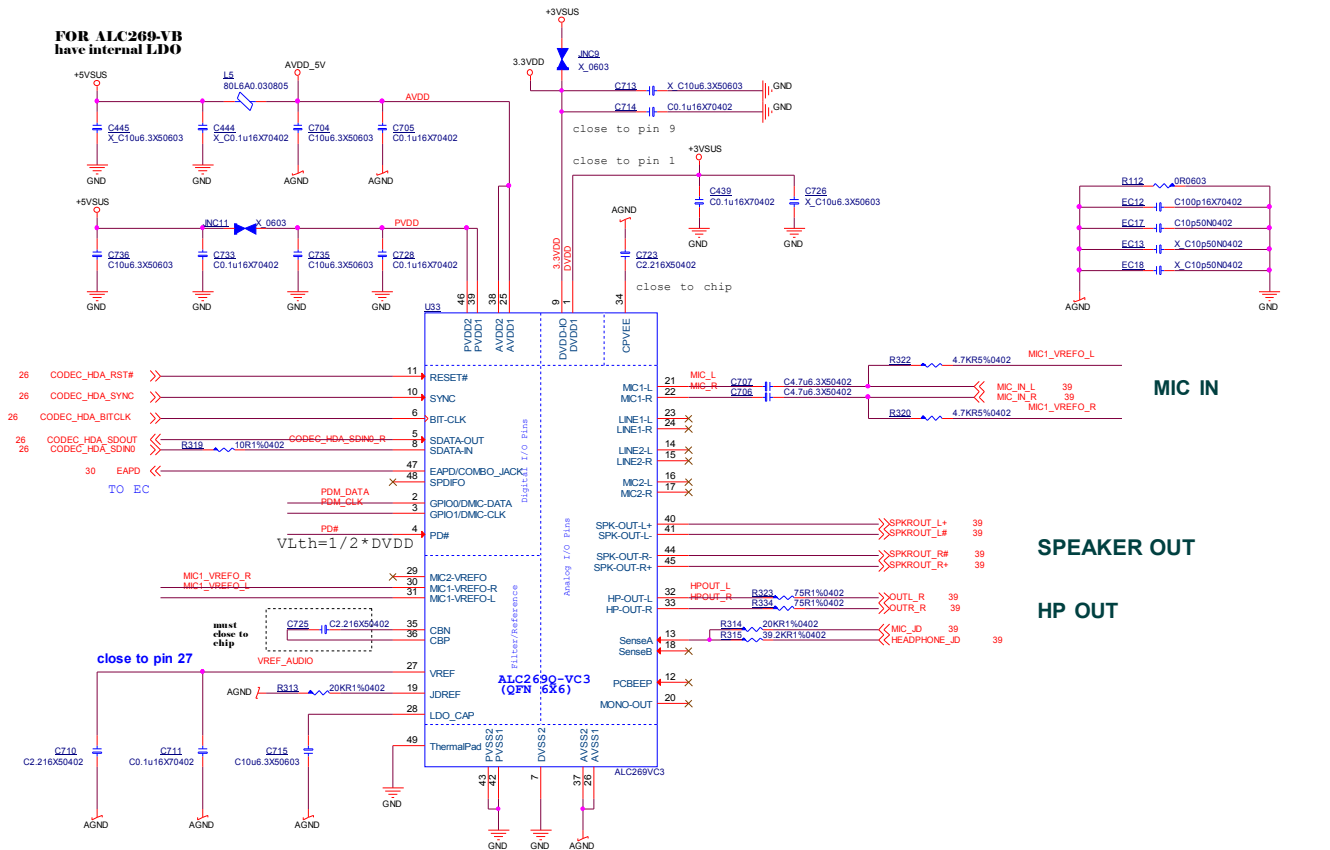
HDD



N5N-22F0401-AF2

		MICRO-STAR INT'L CO.,LTD.	
Title			
M.2 SSD/HDD			
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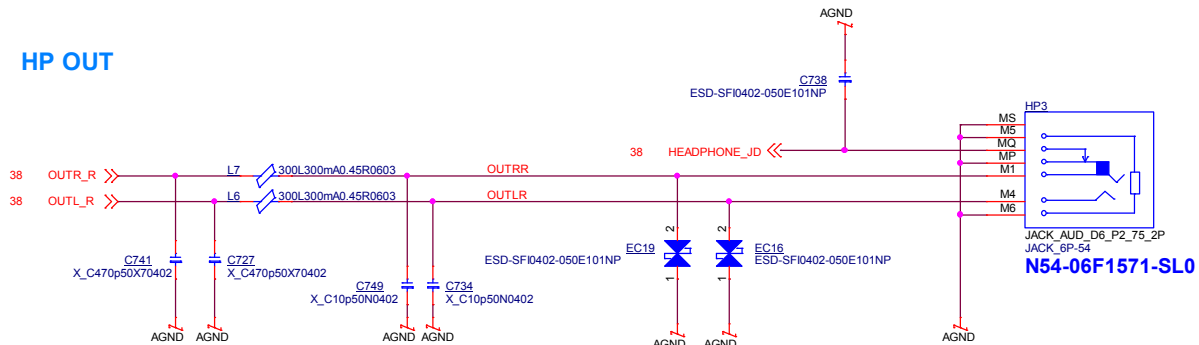
**FOR ALC269-VB
have internal LDO**



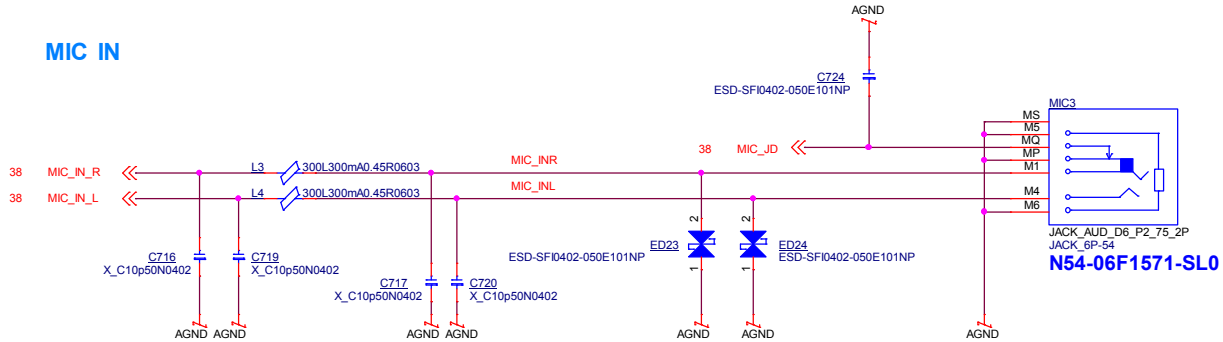
msi MICRO-STAR INT'L CO.,LTD.			
Title			
Audio Codec(ALC269)			
Size	Document	Number	Rev
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Audio CONN

HP OUT

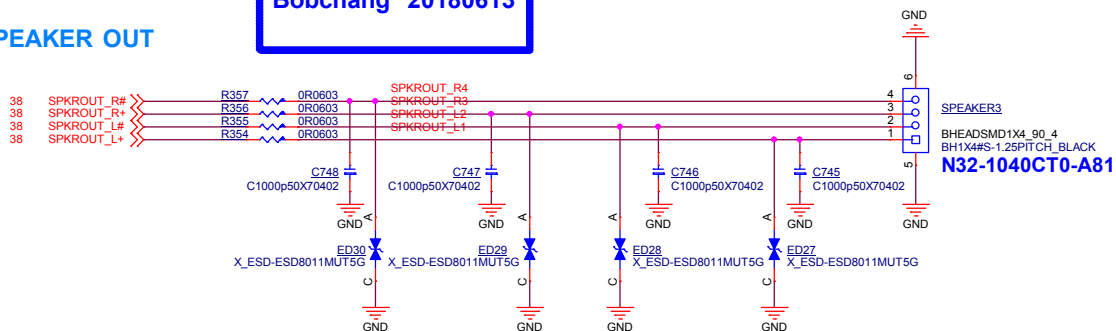


MIC IN



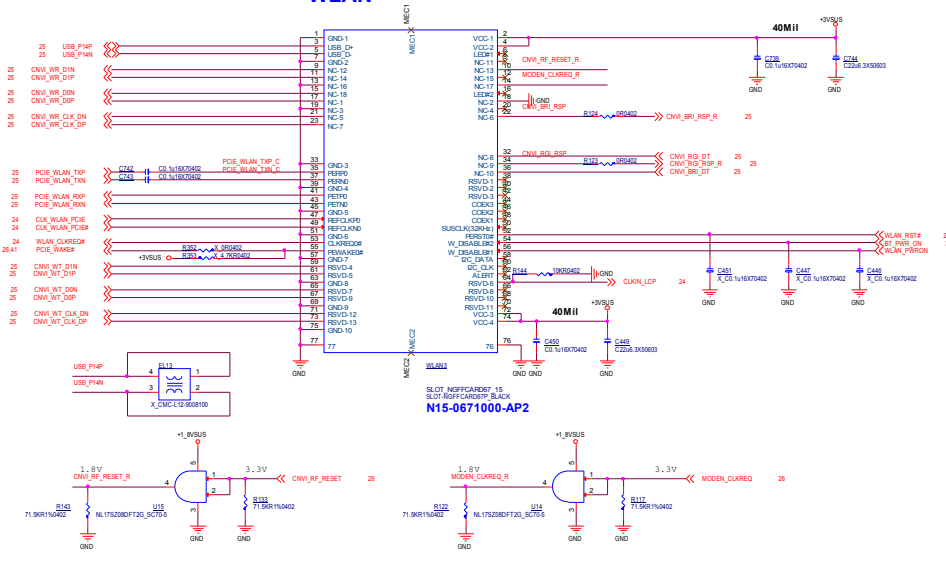
SPEAKER OUT

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WLAN



Pin #	M.2 WLAN	INTEL CNV1 WLAN	Pin #	M.2 WLAN	INTEL CNV1 WLAN
Pin 1	GND	GND	Pin 2	3.3V	3.3V
Pin 3	USB_D+	N/C	Pin 4	3.3V	3.3V
Pin 5	USB_D-	N/C	Pin 6	LED1#	LED1#
Pin 7	GND	GND	Pin 8	Module Key	N/C
Pin 9	Module Key	WGR_D1N	Pin 10	Module Key	RF_RESET_B(1.8V)
Pin 11	Module Key	WGR_D1P	Pin 12	Module Key	N/C
Pin 13	Module Key	GND	Pin 14	Module Key	CLKREQ0(1.8V)
Pin 15	Module Key	WGR_D0N	Pin 16	LED2#	N/C
Pin 17	N/C	WGR_D0P	Pin 18	GND	GND
Pin 19	N/C	GND	Pin 20	N/C	N/C
Pin 21	N/C	WGR_CLKN	Pin 22	N/C	BR1_RSP(1.8V)
Pin 23	N/C	WGR_CLKP	Pin 24	Module Key	Module Key
Pin 25	Module Key	Module Key	Pin 26	Module Key	Module Key
Pin 27	Module Key	Module Key	Pin 28	Module Key	Module Key
Pin 29	Module Key	Module Key	Pin 30	Module Key	Module Key
Pin 31	Module Key	Module Key	Pin 32	N/C	RGL_DT(1.8V)
Pin 33	GND	GND	Pin 34	N/C	RGL_RSP(1.8V)
Pin 35	PERP0	N/C	Pin 36	N/C	BGL_DT(1.8V)
Pin 37	PERP0	N/C	Pin 38	N/C	N/C
Pin 39	GND	GND	Pin 40	N/C	N/C
Pin 41	PETP0	N/C	Pin 42	N/C	N/C
Pin 43	PETP0	N/C	Pin 44	N/C	N/C
Pin 45	GND	GND	Pin 46	N/C	N/C
Pin 47	REFCLKP0	N/C	Pin 48	N/C	N/C
Pin 49	REFCLKN0	N/C	Pin 50	SUSCLK (32KHz)	SUSCLK (32KHz)
Pin 51	GND	GND	Pin 52	PERST0#	N/C
Pin 53	CLKREQ0#	N/C	Pin 54	BT_EN (W_DISABLE2#)	BT_EN (W_DISABLE2#)
Pin 55	PEWAKE0#	N/C	Pin 56	WLAN_EN (W_DISABLE2#)	WLAN_EN (W_DISABLE2#)
Pin 57	GND	GND	Pin 58	N/C	N/C
Pin 59	N/C	WT_D1N	Pin 60	N/C	N/C
Pin 61	N/C	WT_D1P	Pin 62	N/C	N/C
Pin 63	GND	GND	Pin 64	Resever	REFCLK0(38.4MKz)
Pin 65	N/C	WT_D0N	Pin 66	N/C	N/C
Pin 67	N/C	WT_D0P	Pin 68	N/C	N/C
Pin 69	GND	GND	Pin 70	N/C	N/C
Pin 71	N/C	WT_CLKN	Pin 72	3.3V	3.3V
Pin 73	N/C	WT_CLKP	Pin 74	3.3V	3.3V
Pin 75	GND	GND			

msi

MICRO-STAR INT'L CO.,LTD.

File

WLAN

Size

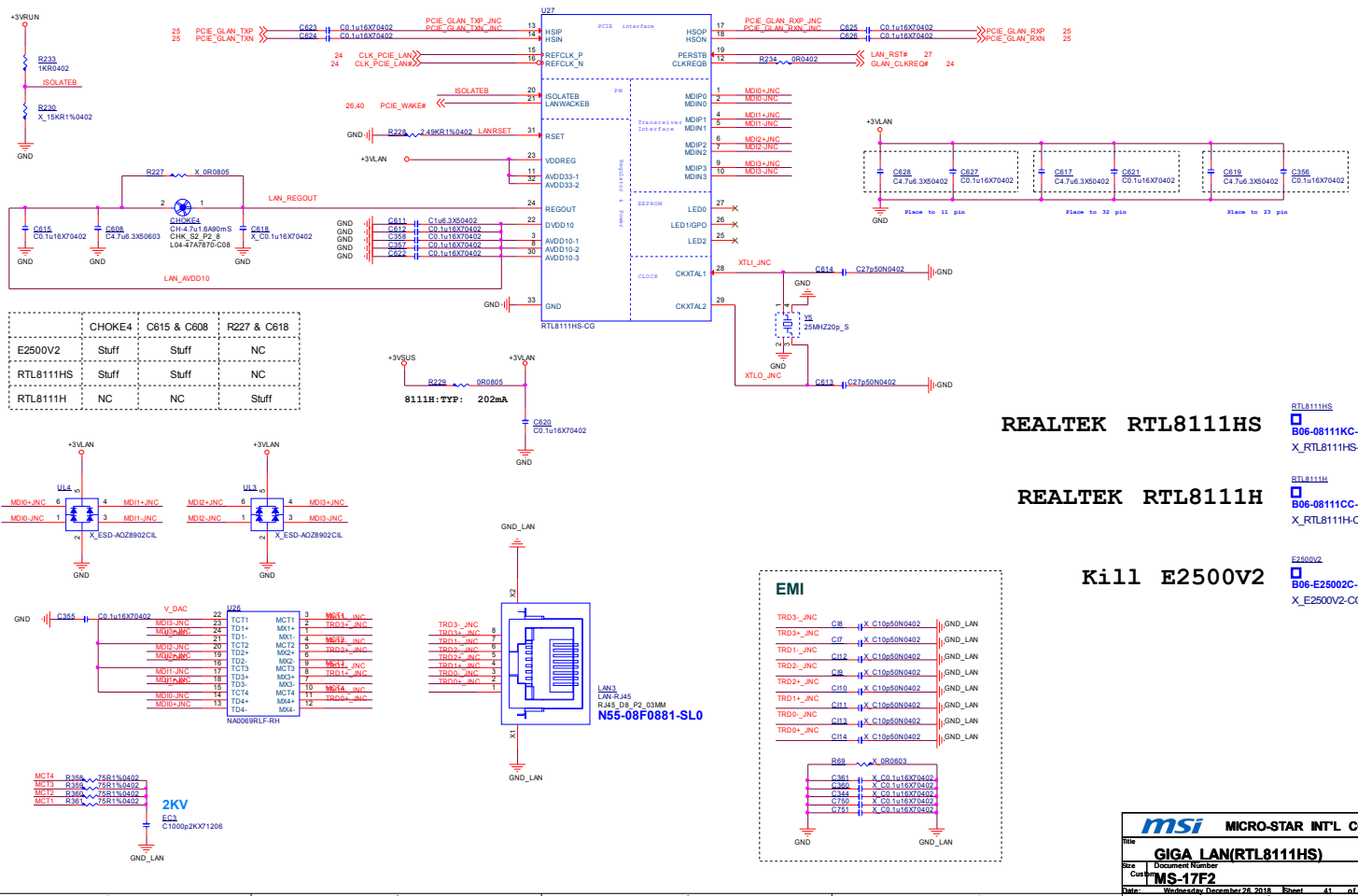
MS-17F2

Date

Wednesday, December 26, 2018

Rev

1.0



REALTEK RTL8111HS

RTL8111HS
B06-08111KC-R09
X_RTL8111HS-CG

REALTEK RTL8111H

RTL8111H
B06-08111CC-R09
X_RTL8111H-CG

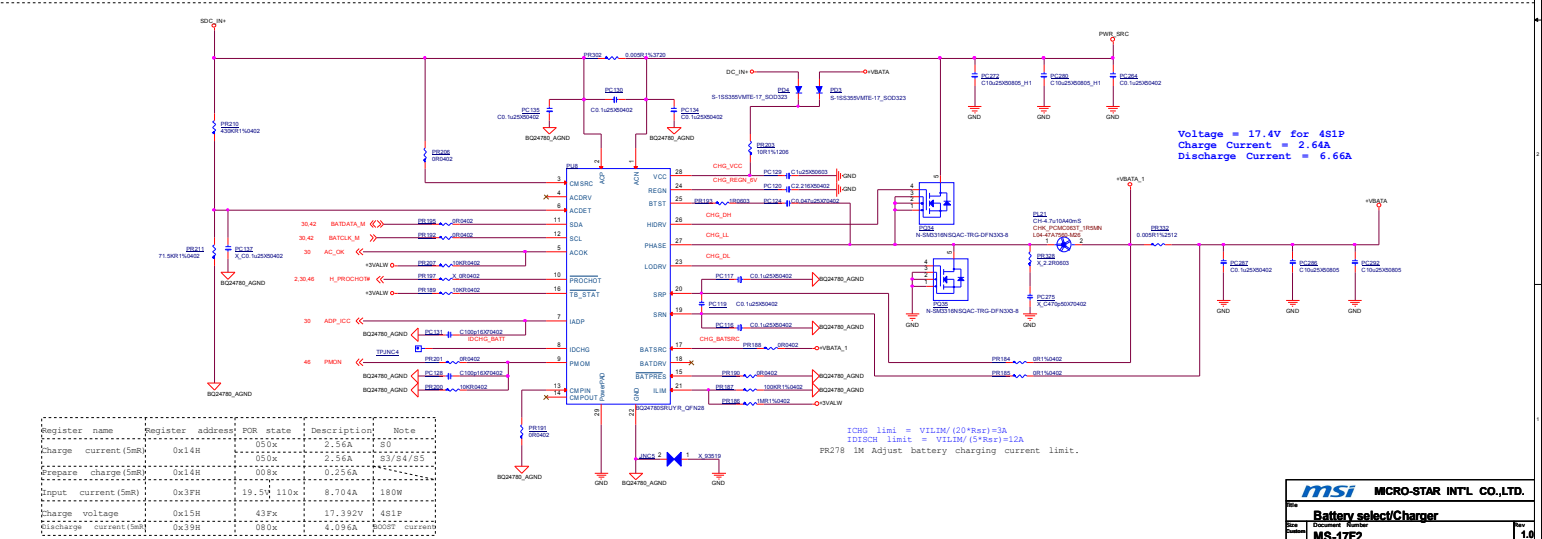
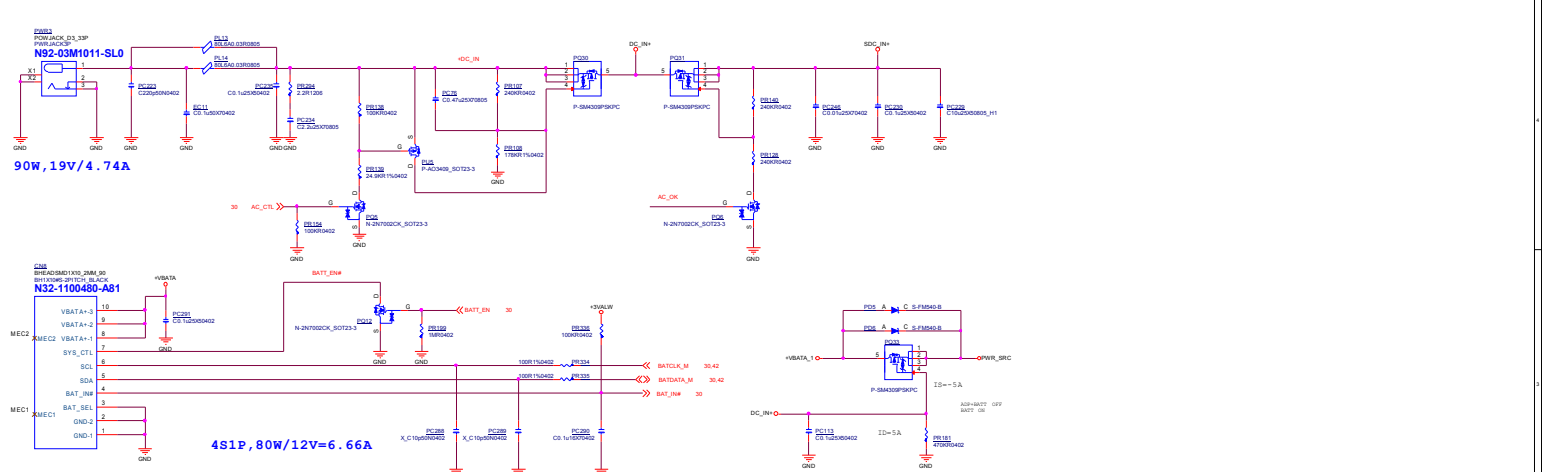
Kill E2500V2

E2500V2

B06-E25002C-R54
X_E2500V2-CG

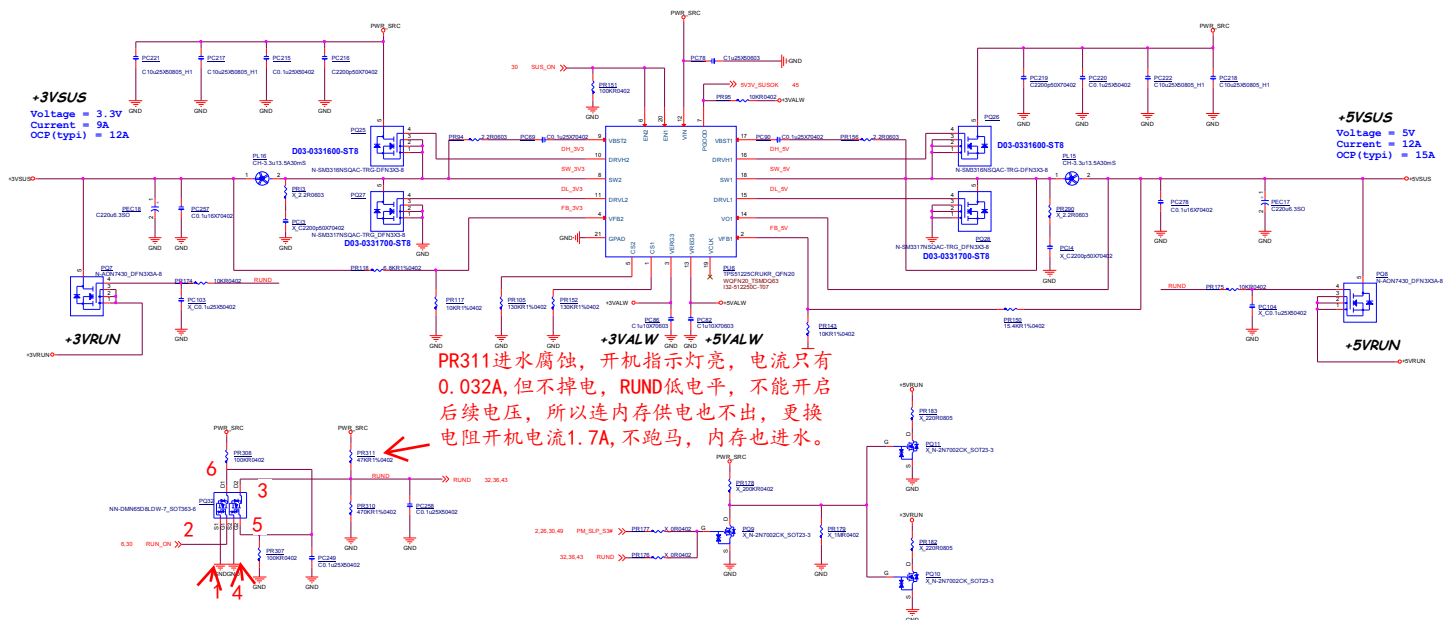
EMI

RJ45_D8_P2_03MM
N55-08F0881-SL0

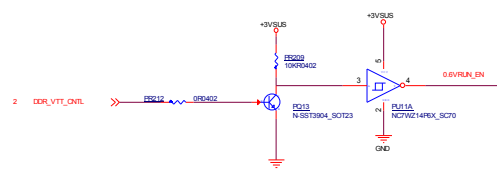
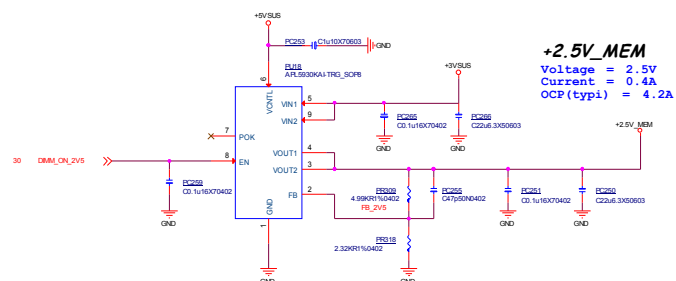


Register	Name	Register address	FOR	State	Description	Note
Charge current	(mA)	0x14H	055H	0.55A	2.56A	43/54/55
Discharge current	(mA)	0x15H	055H	0.55A	2.56A	43/54/55
Input current	(mA)	0x3FH	19.5W	110x	8.704A	1.80W
Charge voltage	(V)	0x16H	43Fx	17.392V	481P	
Discharge voltage	(V)	0x38H	055H	0.55A	2.56A	43/54/55

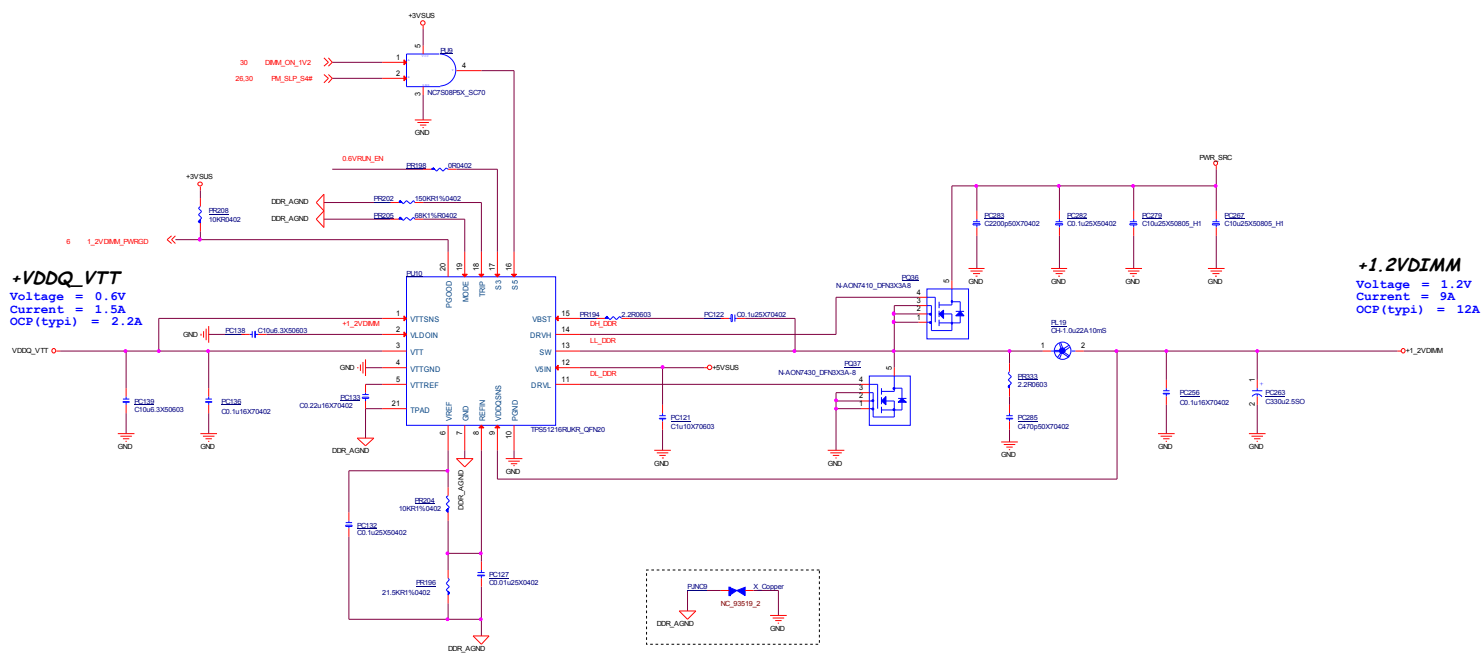
System Power



+2.5V_MEM

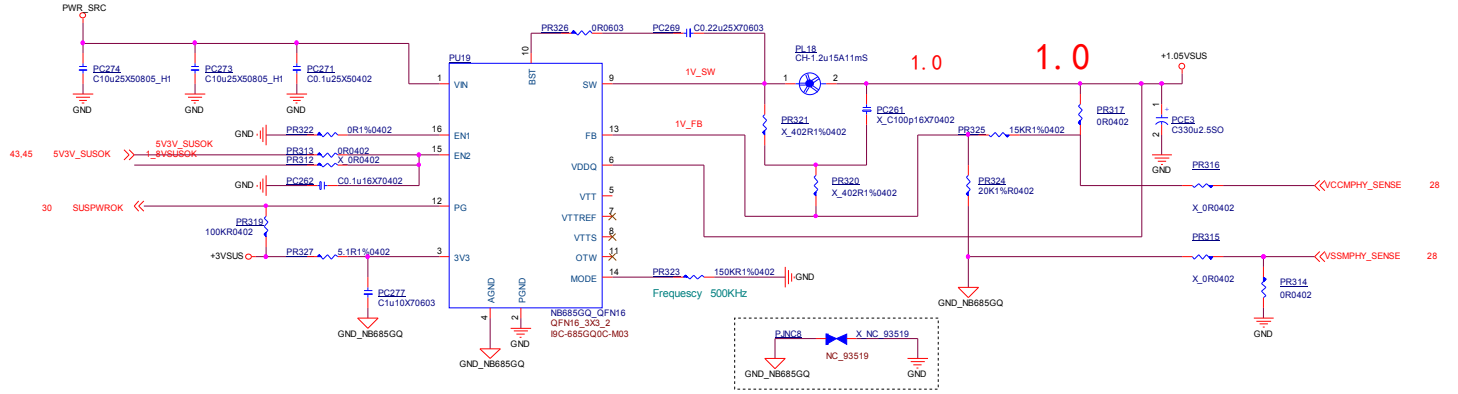


+1.2VDIMM / VDDQ_VTT(0.6V)



+1.05VSUS

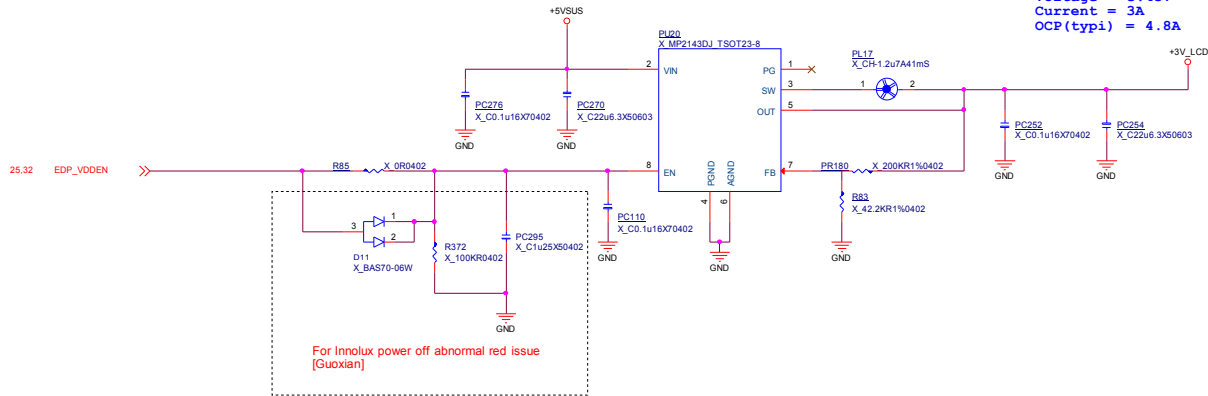
Voltage = 1.05V
Current = 10A
OCP(typi) = 13A



+3V_LCD

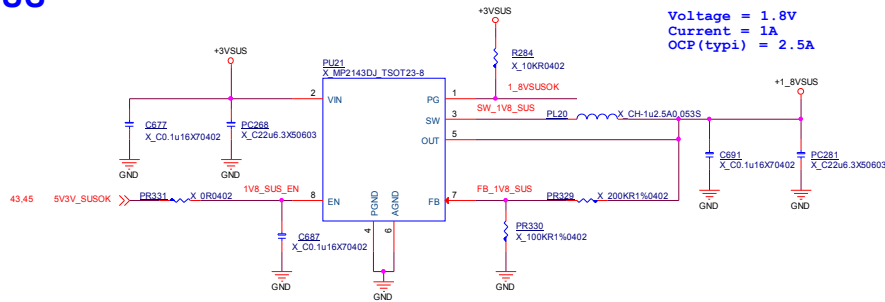
Panel Device Logic Power

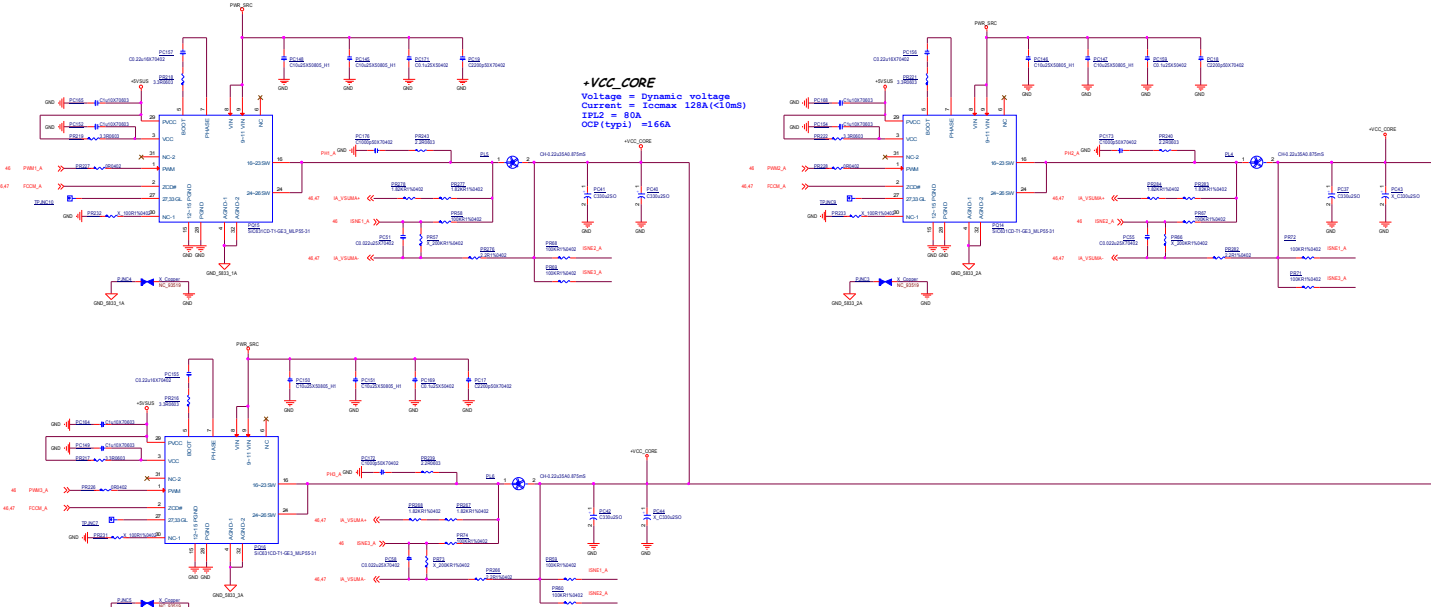
Voltage = 3.45V
Current = 3A
OCP(typi) = 4.8A



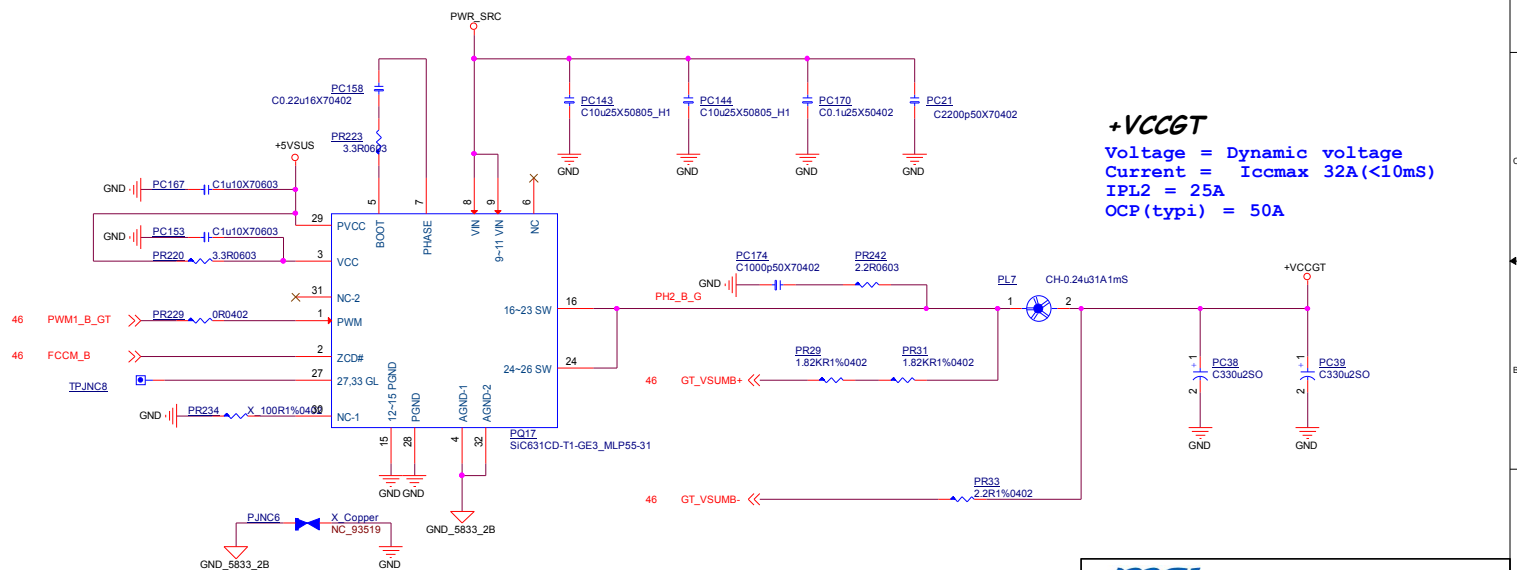
+1_8VSUS

Voltage = 1.8V
Current = 1A
OCP(typi) = 2.5A



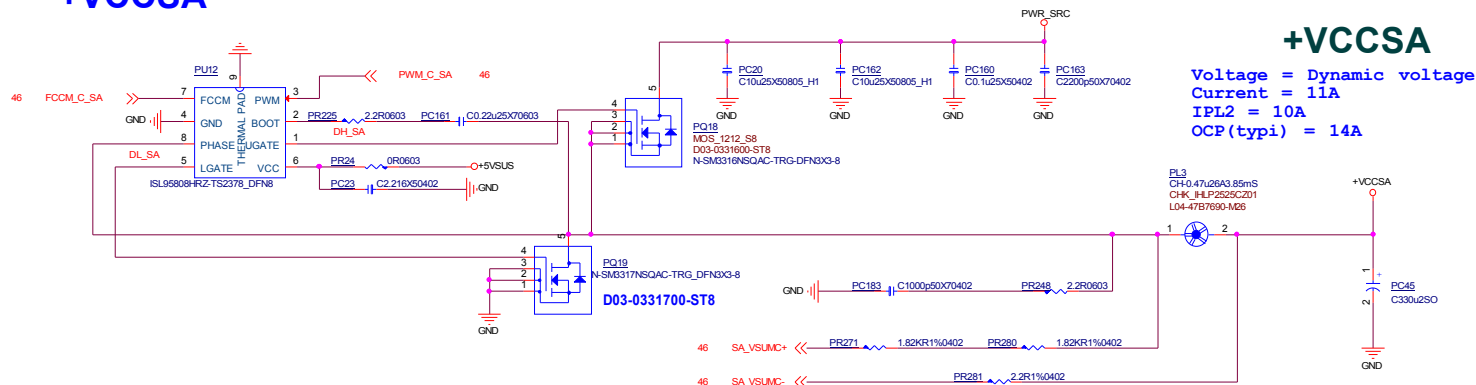


+VCCGT

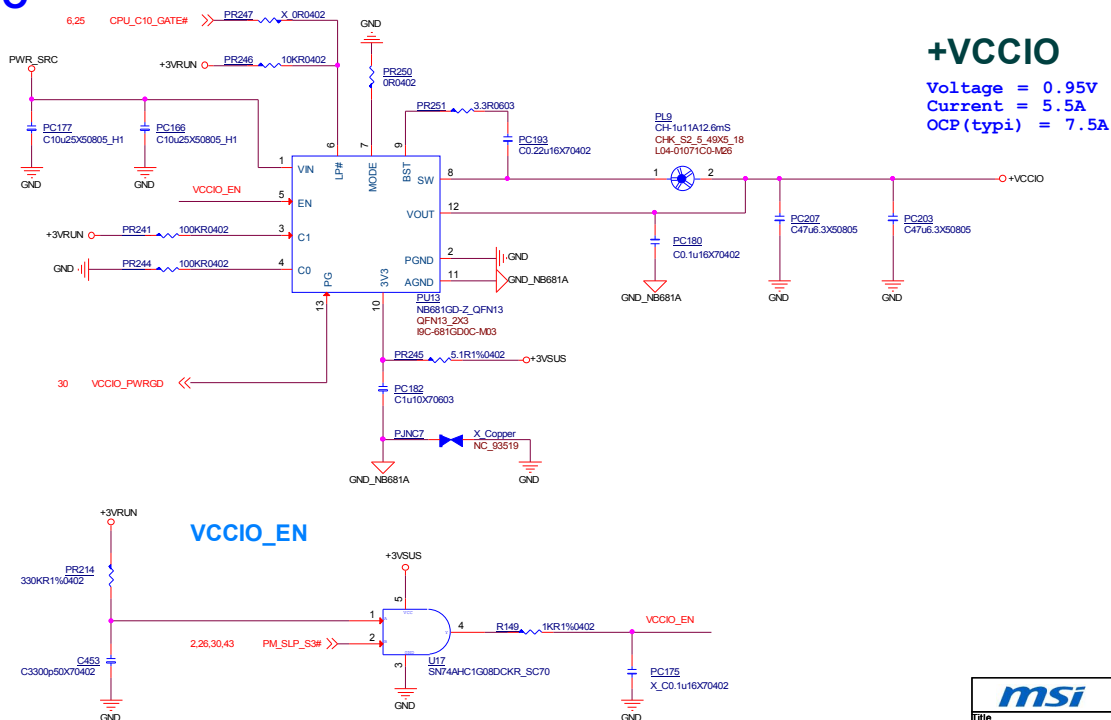


msi MICRO-STAR INT'L CO.,LTD.			
Title			
CPU Power (VCCGT)			
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+VCCSA



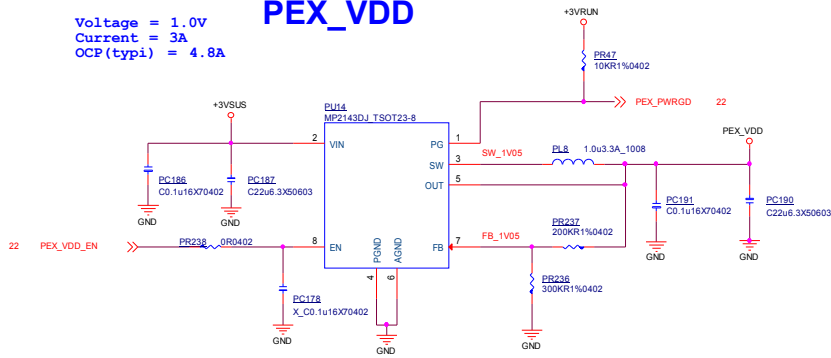
+VCCIO



msi MICRO-STAR INT'L CO.,LTD.			
File			
CPU Power (VCCSA/VCCIO)			
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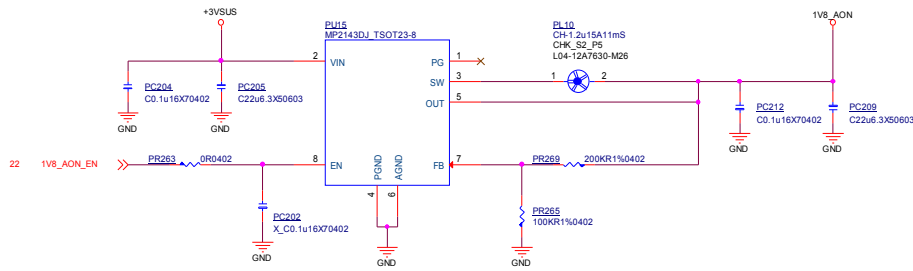
Voltage = 1.0V
Current = 3A
OCP(typi) = 4.8A

PEX_VDD

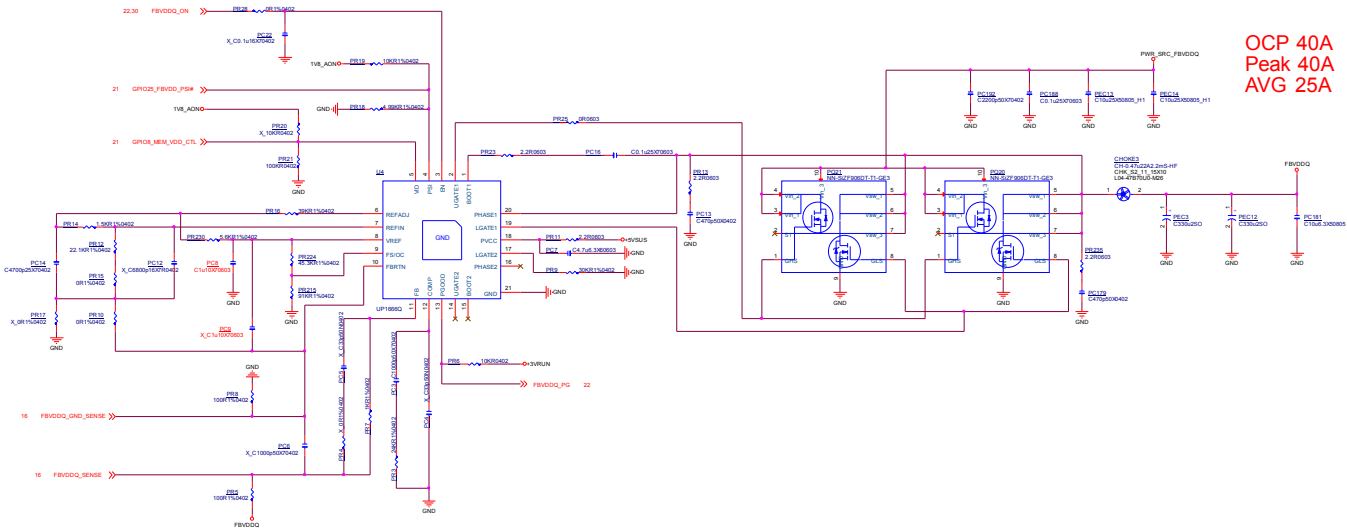


1V8_AON

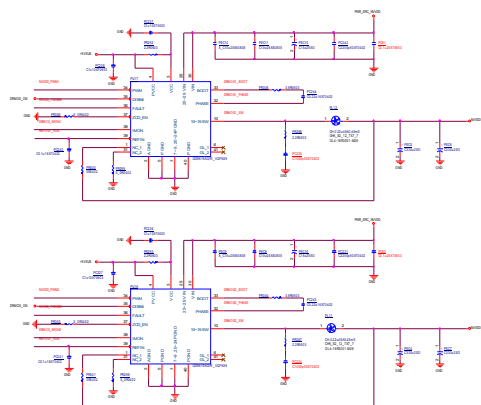
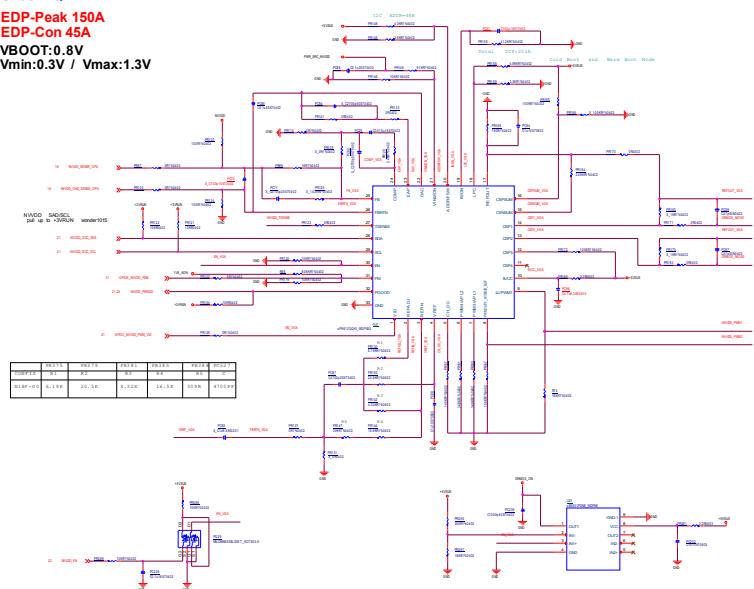
Voltage = 1.8V
Current = 2.3A
OCP(typi) = 4.8A



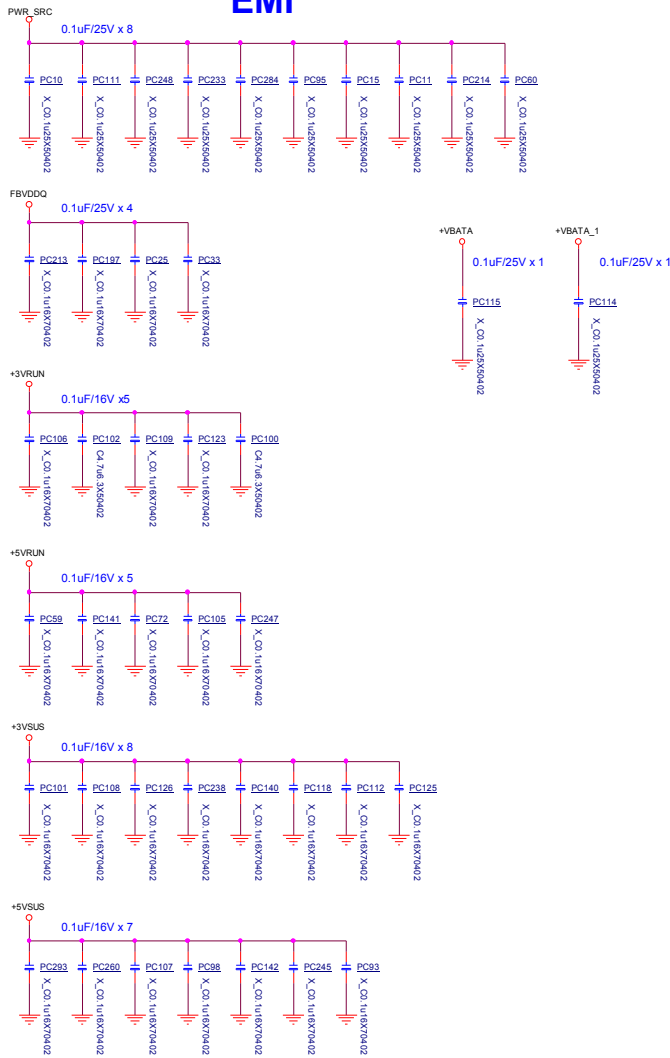
msi MICRO-STAR INT'L CO.,LTD.			
File			
DGPU POWER PEX_VDD/1V8_AON			
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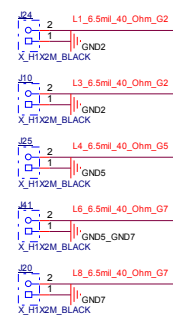
DGPU POWER NVVDD
UP9512Q
EDP-Peak 150A
EDP-Con 45A
VBOOT:0.8V
Vmin:0.3V / Vmax:1.3V



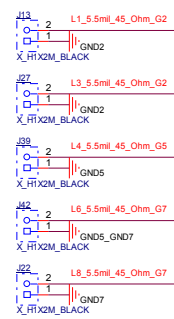
EMI



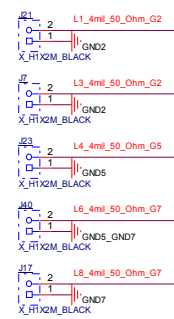
40 OHM Single-End



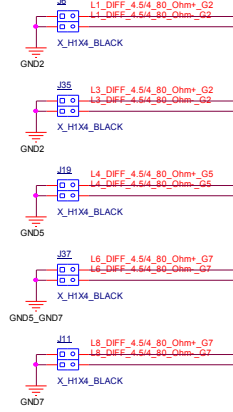
45 OHM Single-End



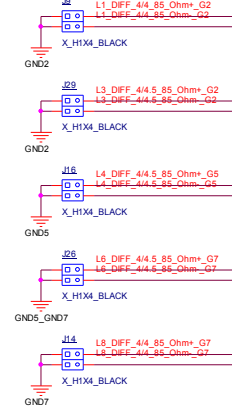
50 OHM Single-End



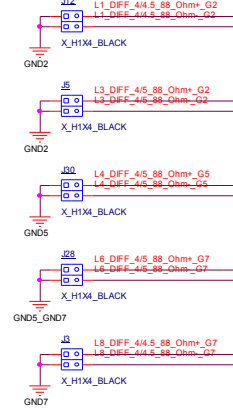
80 OHM Differential



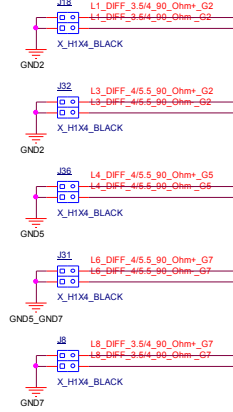
85 OHM Differential



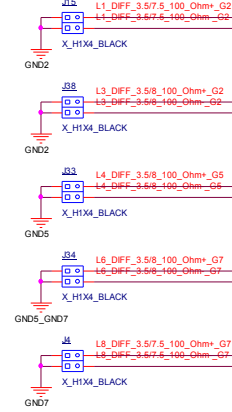
88 OHM Differential



90 OHM Differential

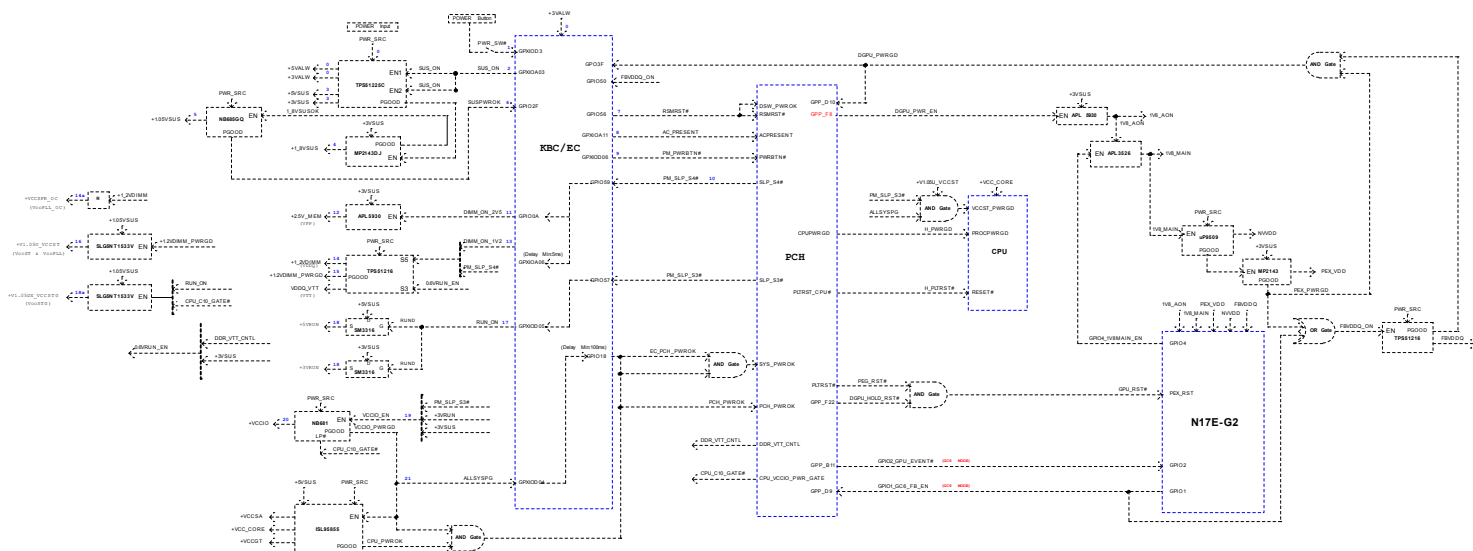


100 OHM Differential

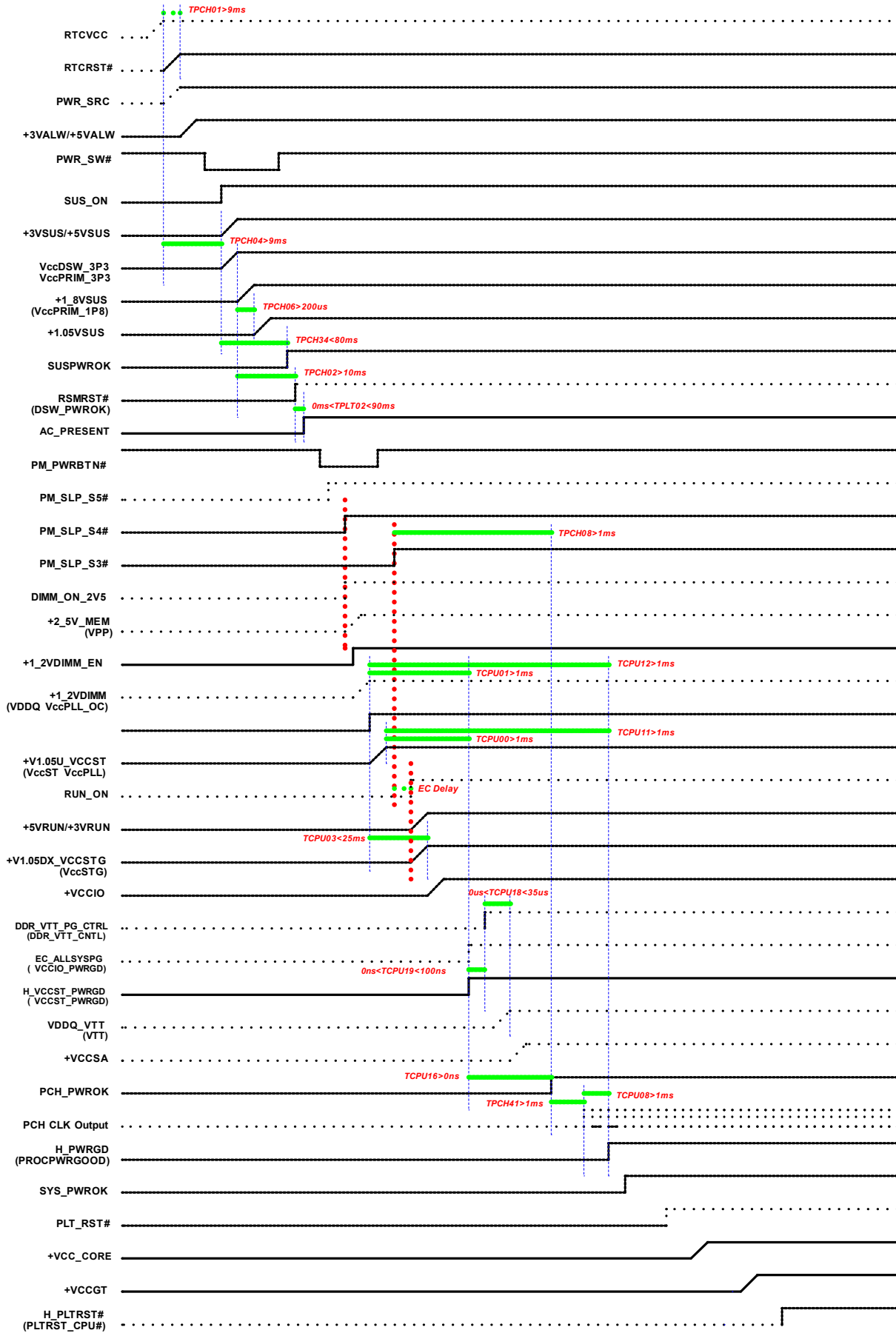


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MS-16R1 Power on Block Diagram



G3 -> S0



S0 -> G3

